

简介

本用户手册主要讲述芯片与烧录转接板的接线说明。

- 本用户手册适用芯圣全系列芯片。
- 相关数据手册、工具及技术文档下载网址: <http://www.holychip.cn/>。

注:

- 1、接线示意图中的红线表示用户需要将红线相连的两个引脚使用 0 欧电阻短接。
- 2、用户也可以将转接板上的引脚全部焊接上排针，烧录不同型号时按照示意图使用跳线帽短接。
- 3、FLASH/TOUCH 芯片的 1 引脚与转接板上的 1 引脚对应摆放。

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1、LINK V4.0/PM51 引脚图

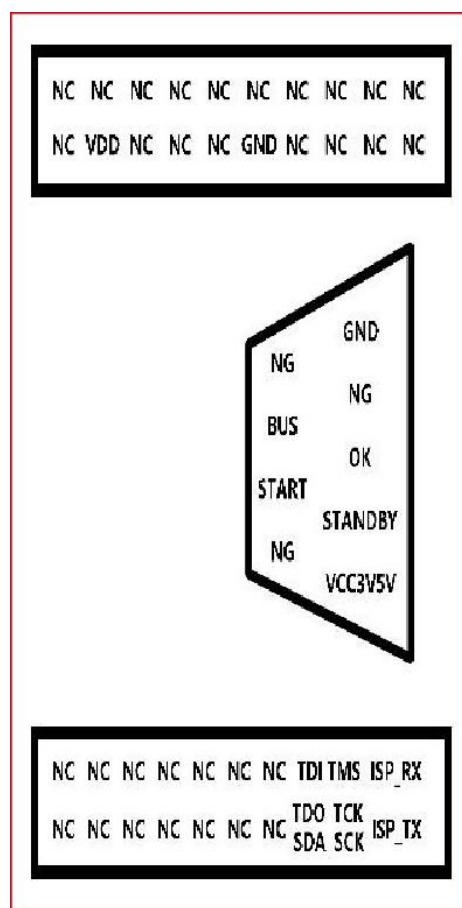
JTAG 烧录引脚: VCC/VDD、TMS、TCK、TDO、TDI、GND

SWD 烧录引脚: VCC/VDD、SCK、SDA、GND

ISP 烧录引脚: VCC/VDD、RX/ISP_RX、TX/ISP_TX、GND



HC-LINK V4.0 脚位图



HC-PM51 V5.0 引脚图

2、FLASH

JTAG 烧录引脚: VDD、TMS、TCK、TDO、TDI、GND

SWD 烧录引脚: VDD、SCK、SDA、GND

ISP 烧录引脚: VDD、ISP_RX、ISP_TX、GND

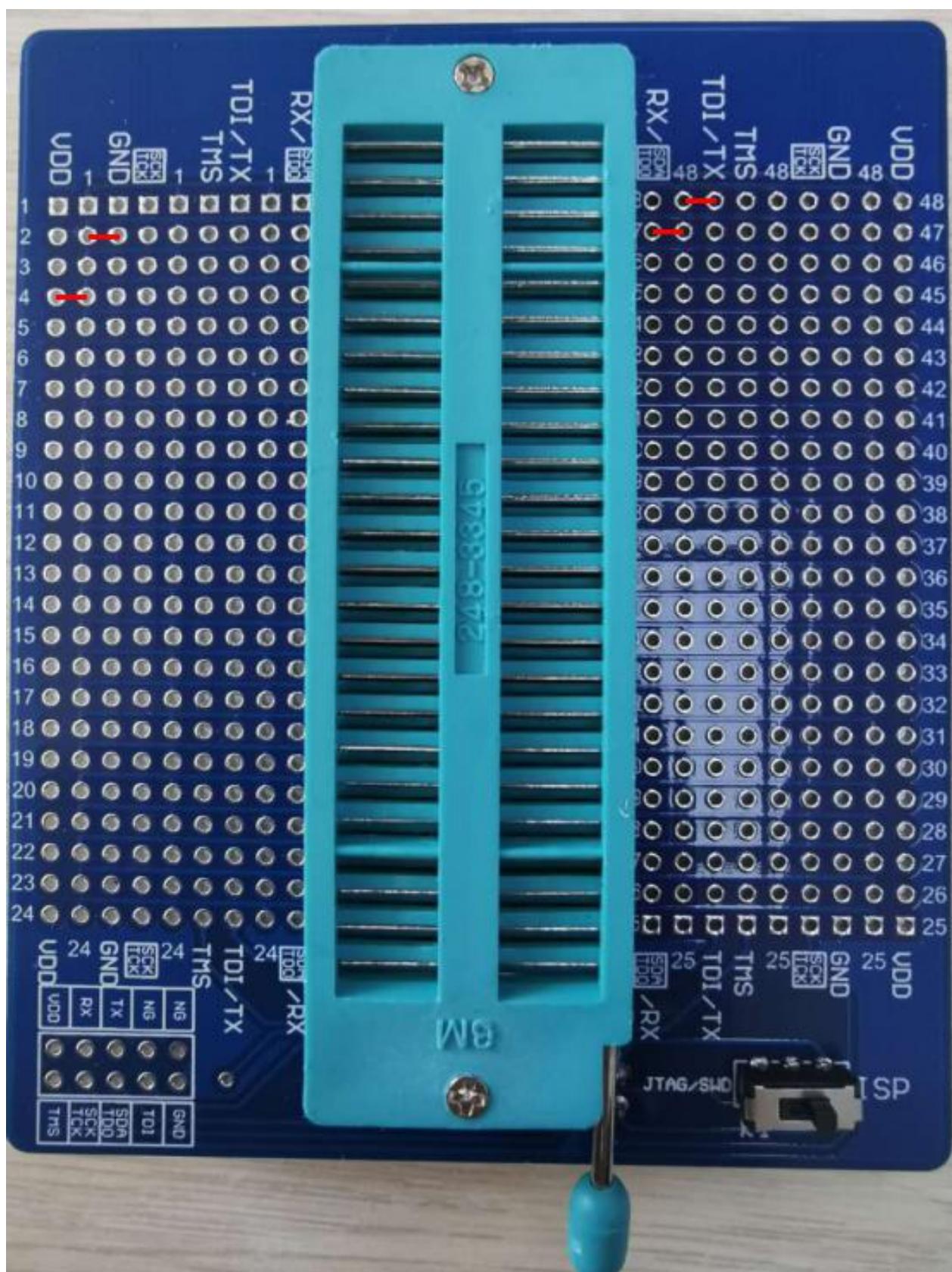
NC
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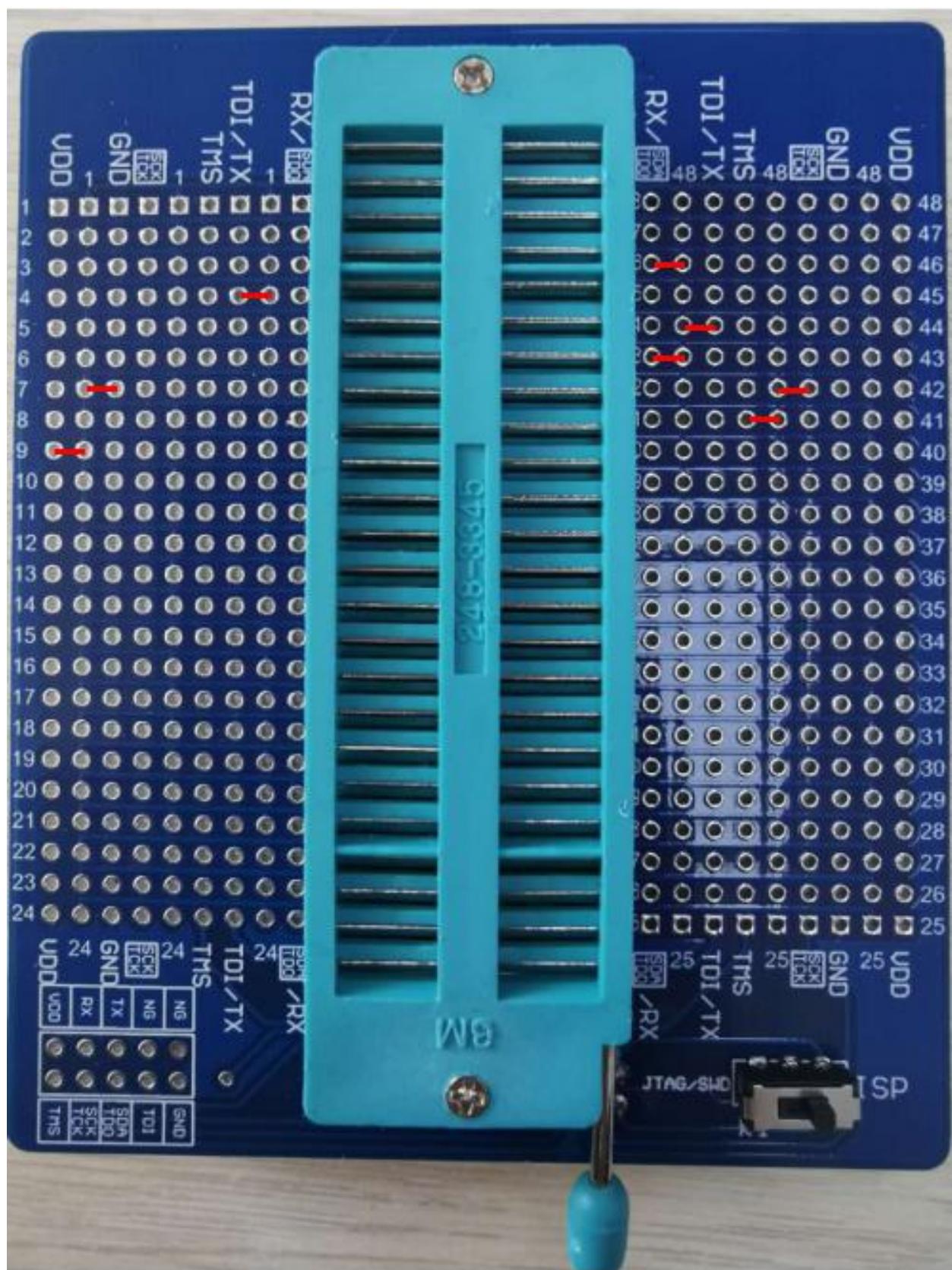
NC NC NC NC NC NC NC TDI TMS ISP_RX
NC NC NC NC NC NC NC TDO TCK SDA SCK ISP_TX

HC-PM51 V5.0 引脚图

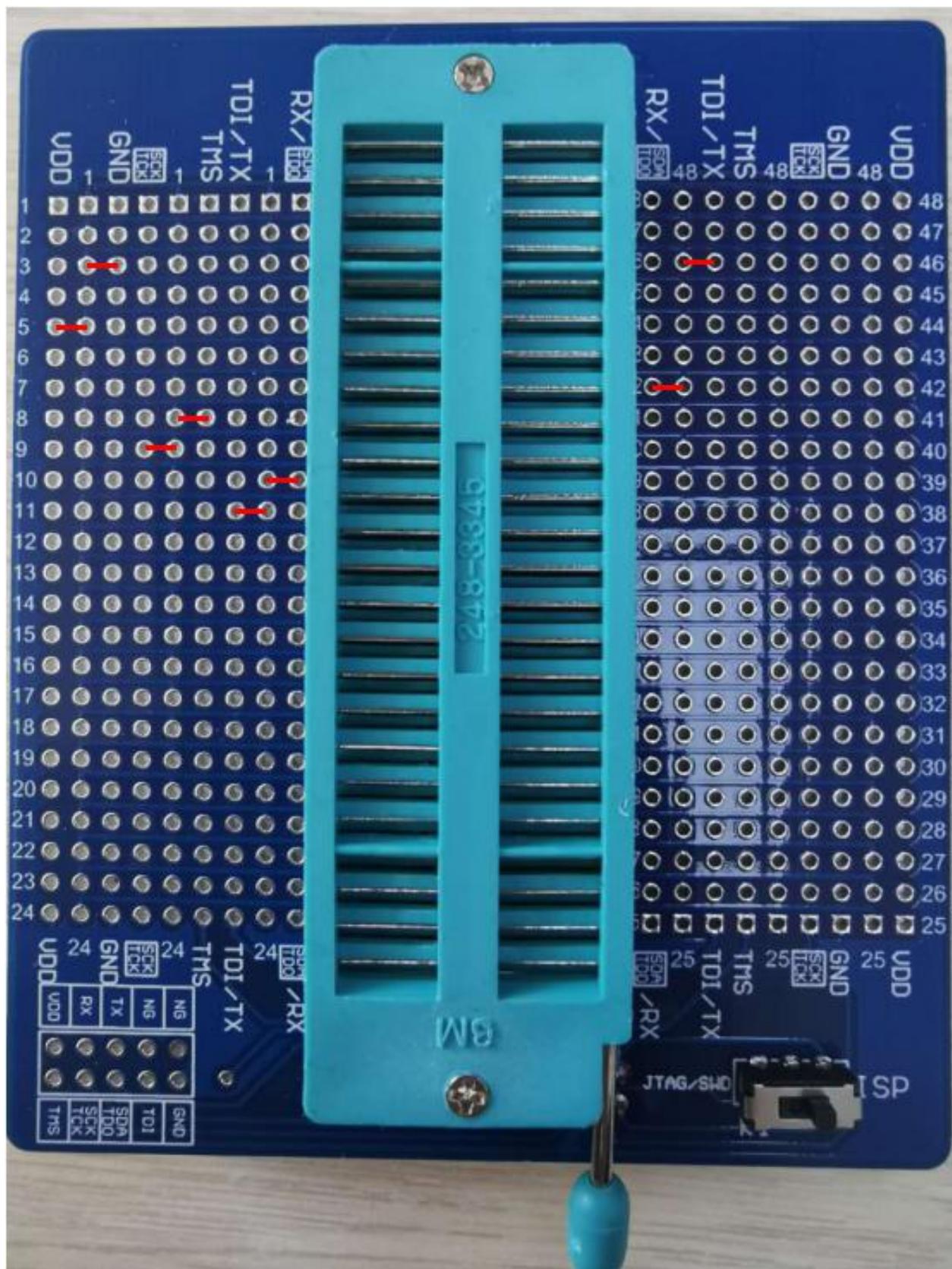
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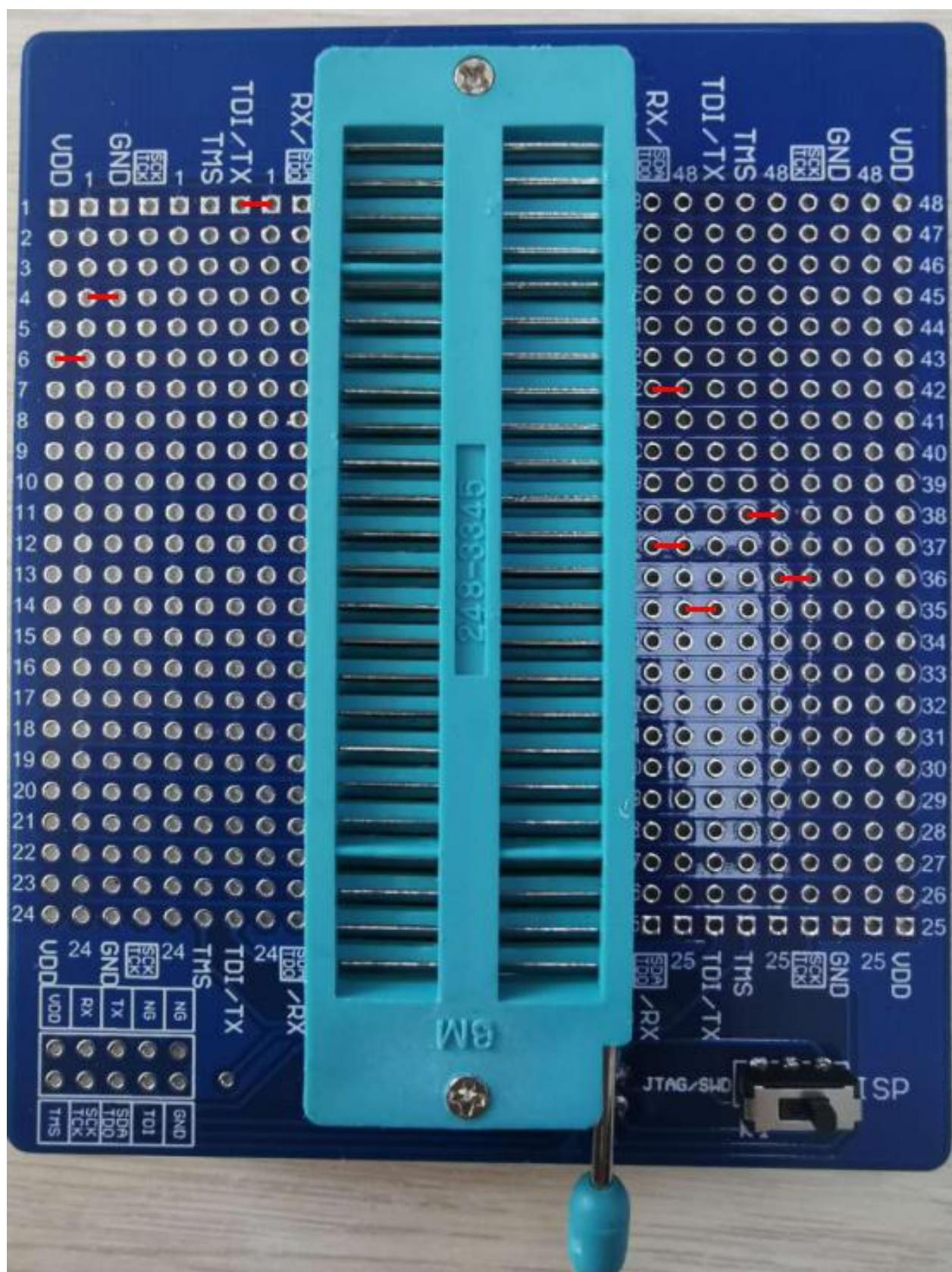
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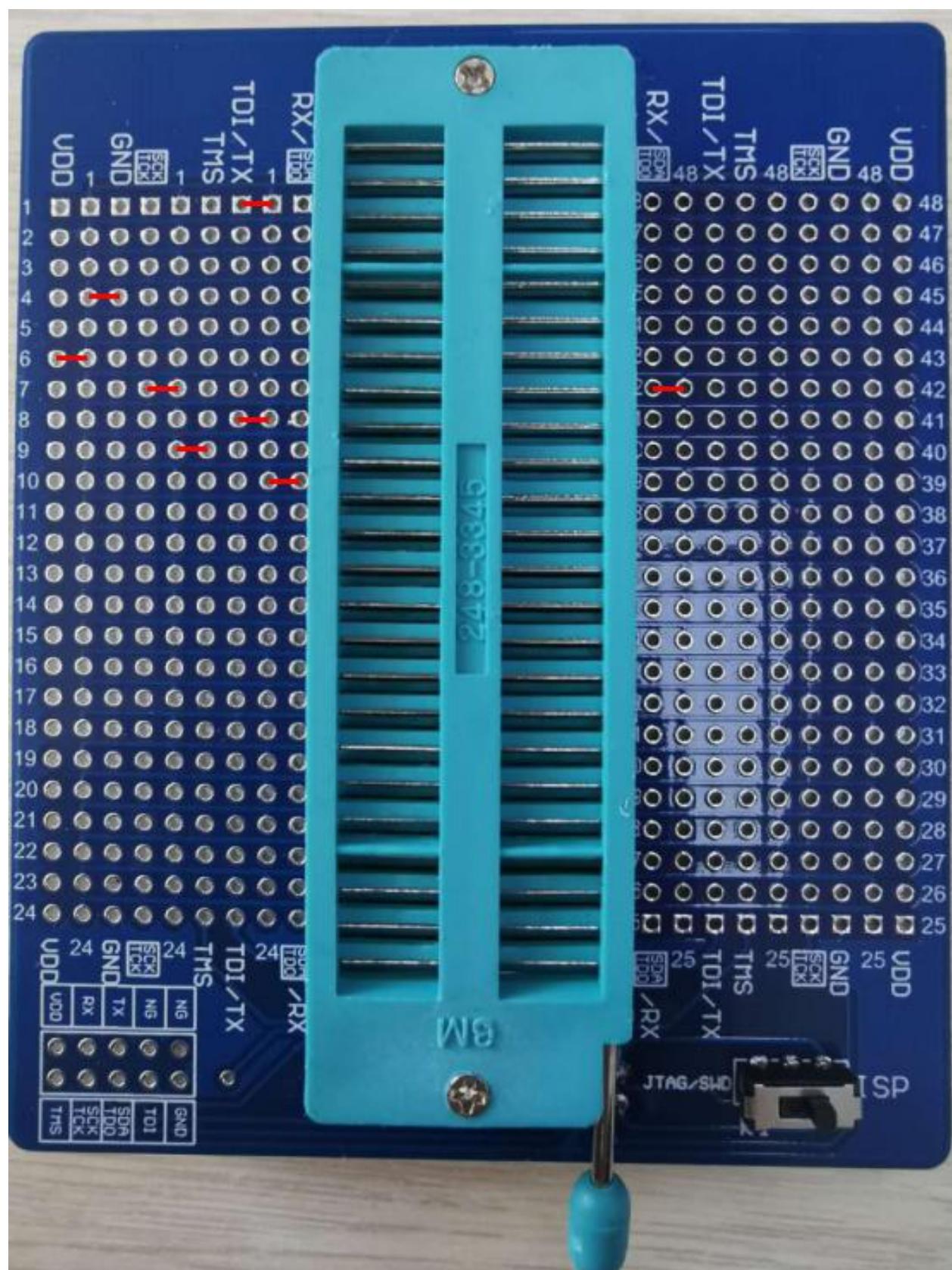
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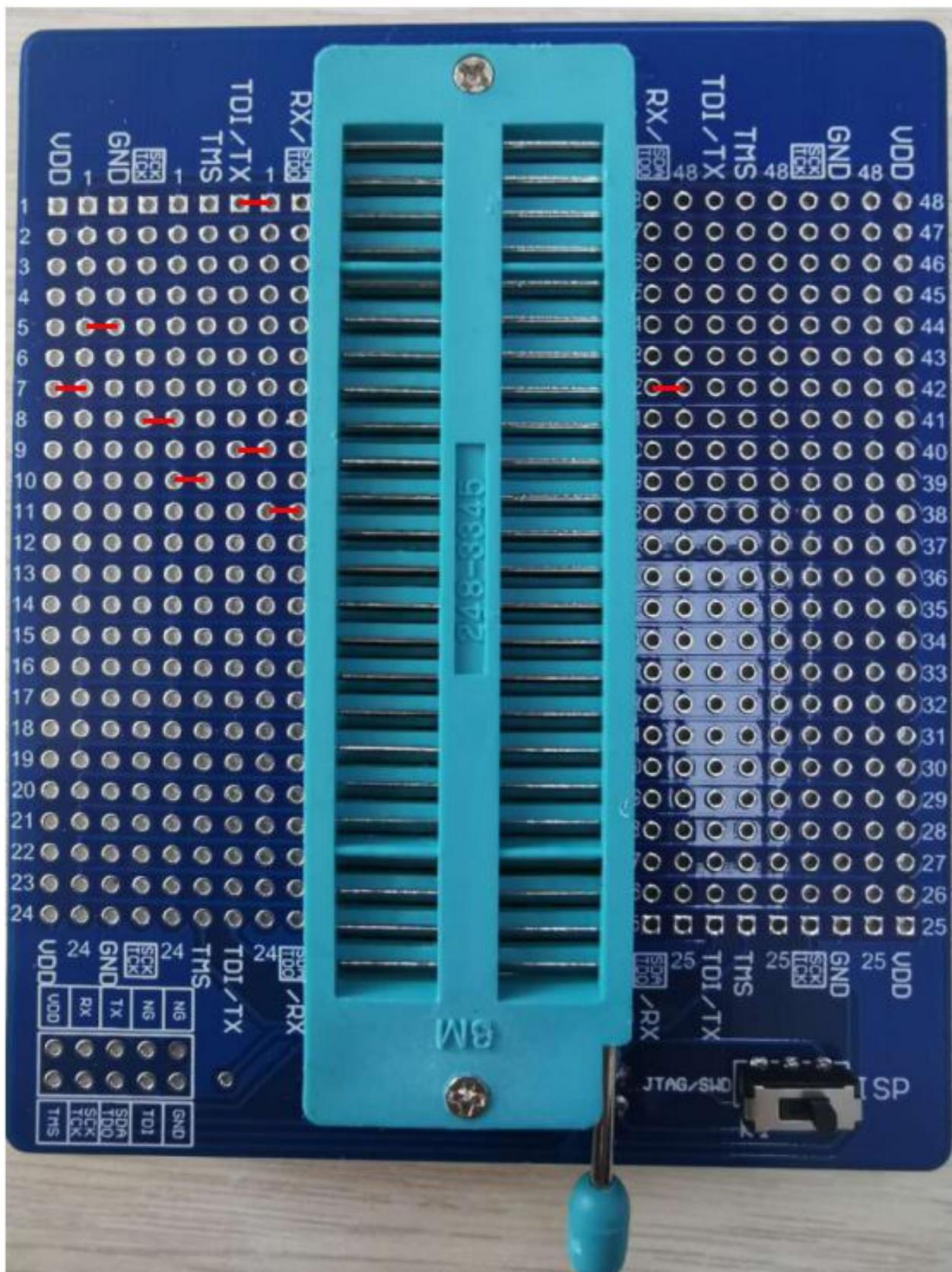
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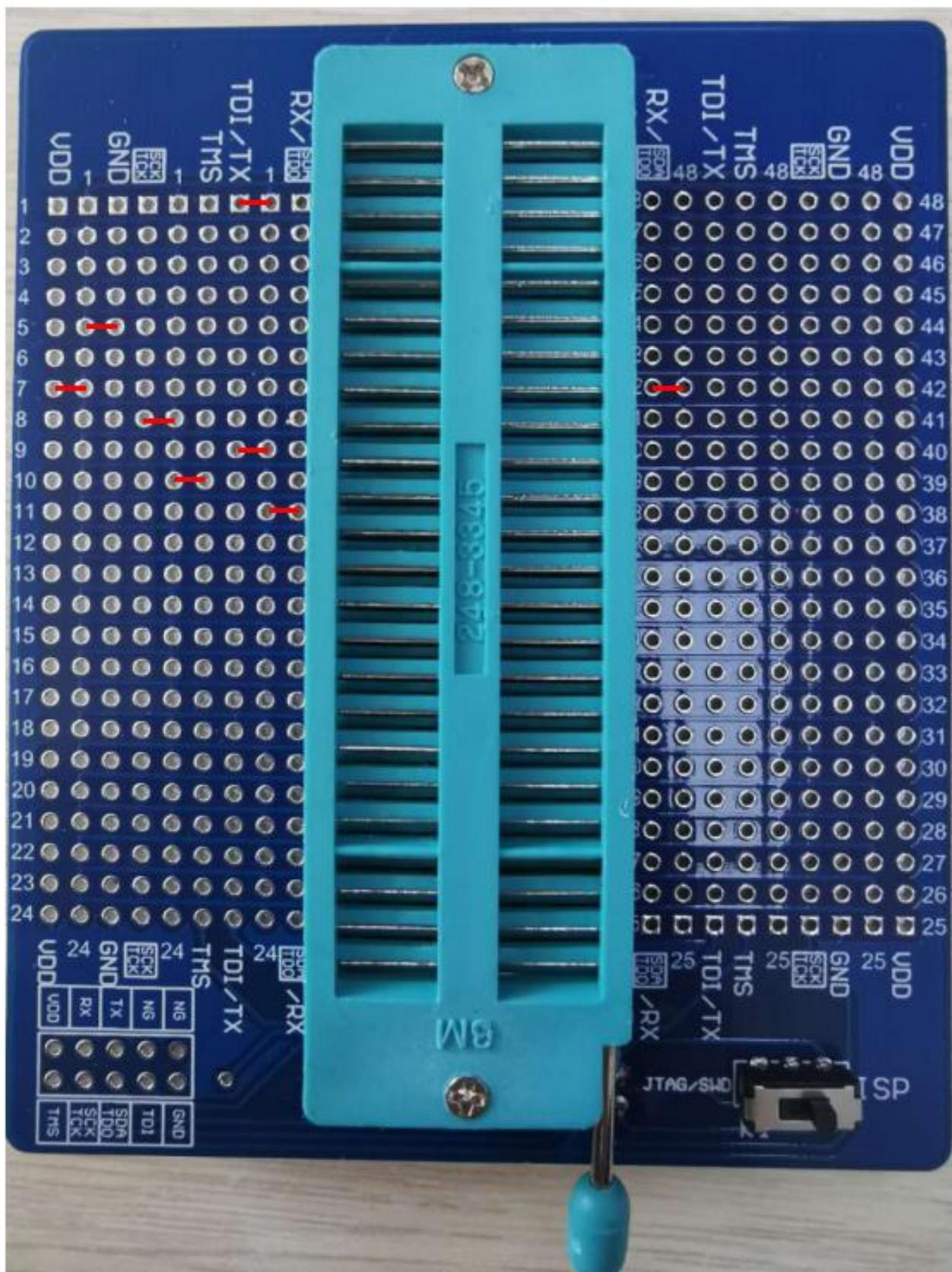
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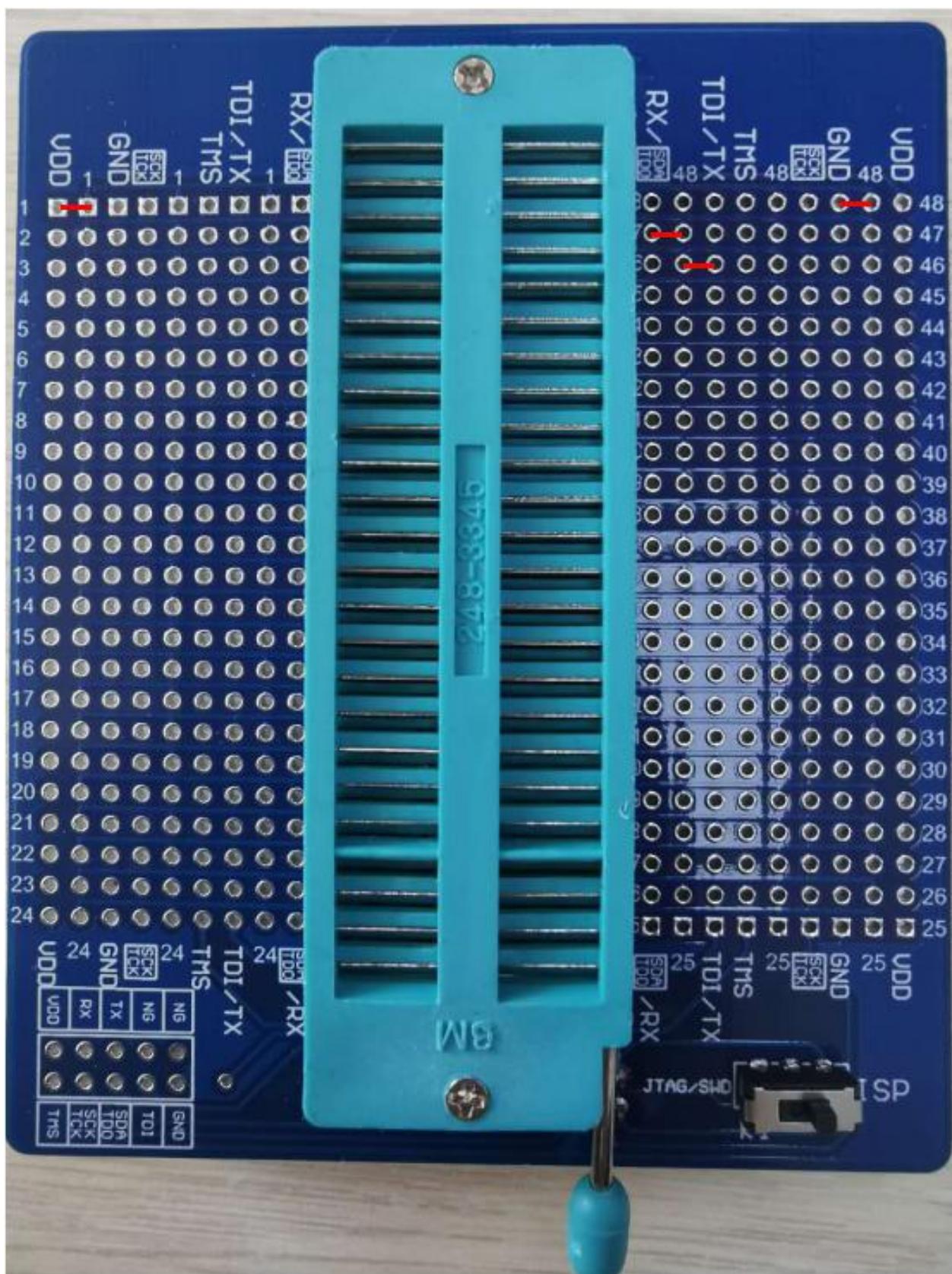
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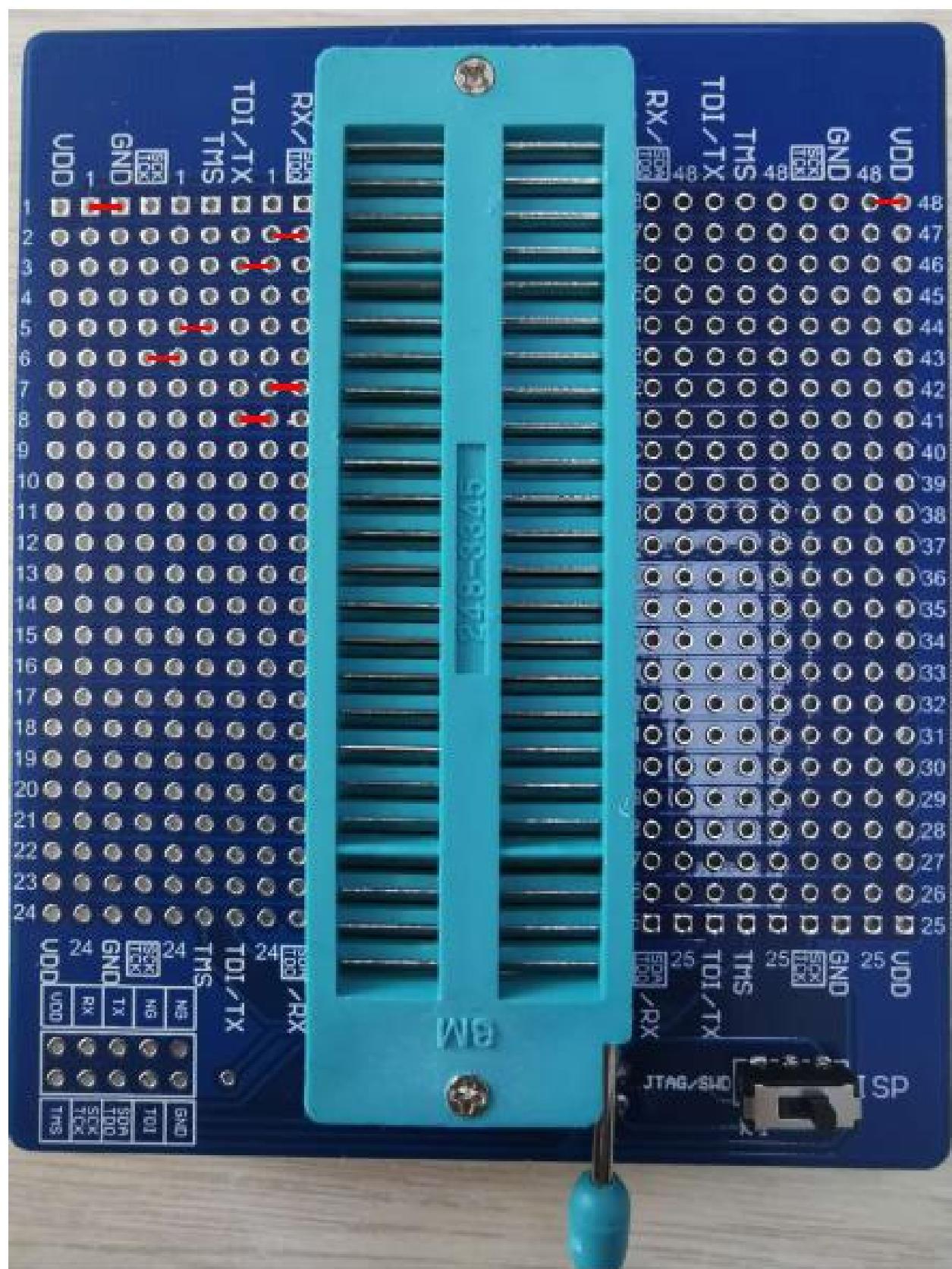
2.7 HC89S105C8-LQFP48-ISP&JTAG



2. 8 HC89F0411P-SOP8-ISP



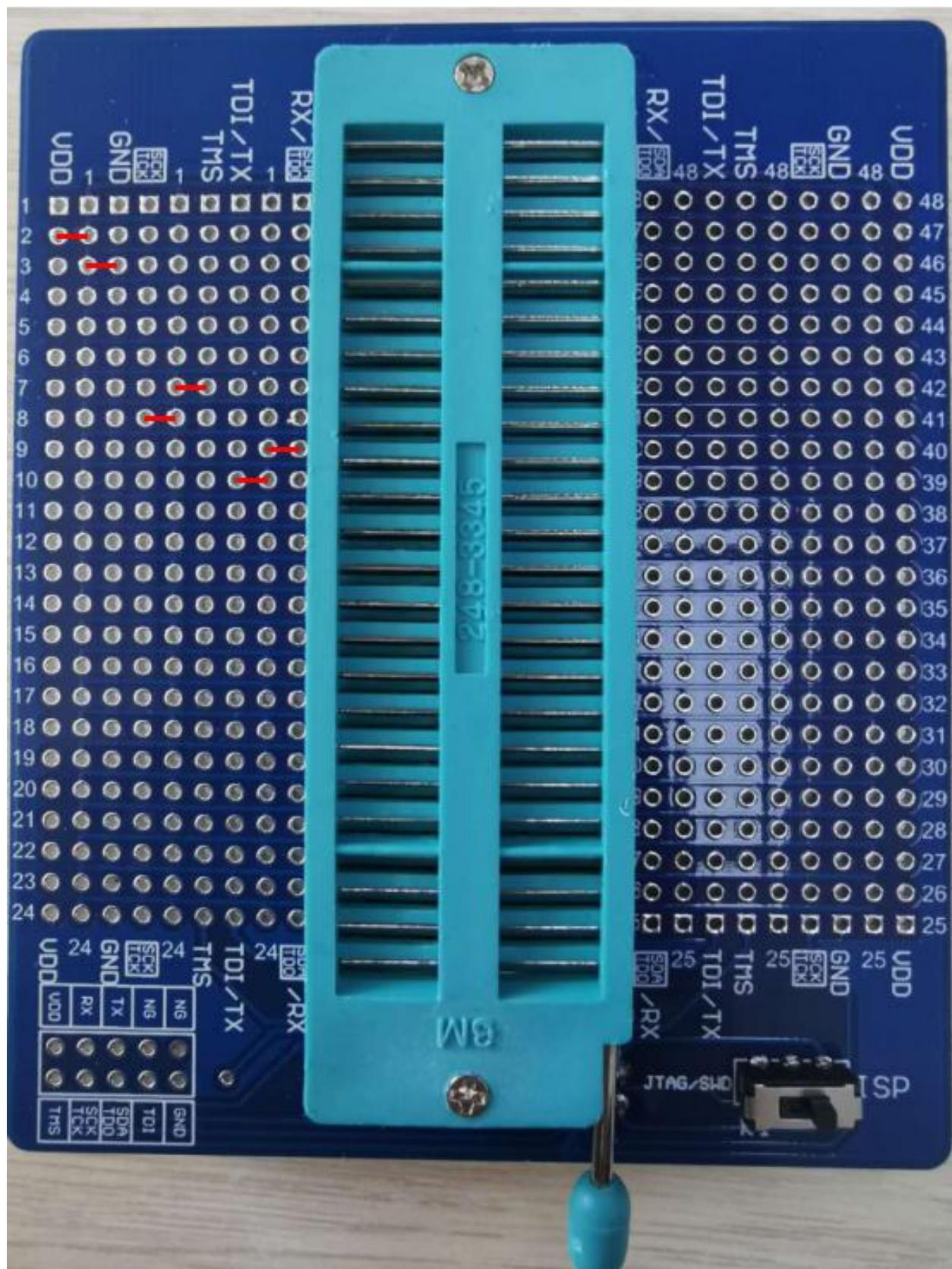
2. 9 HC89F0421-SOP16-ISP&JTAG



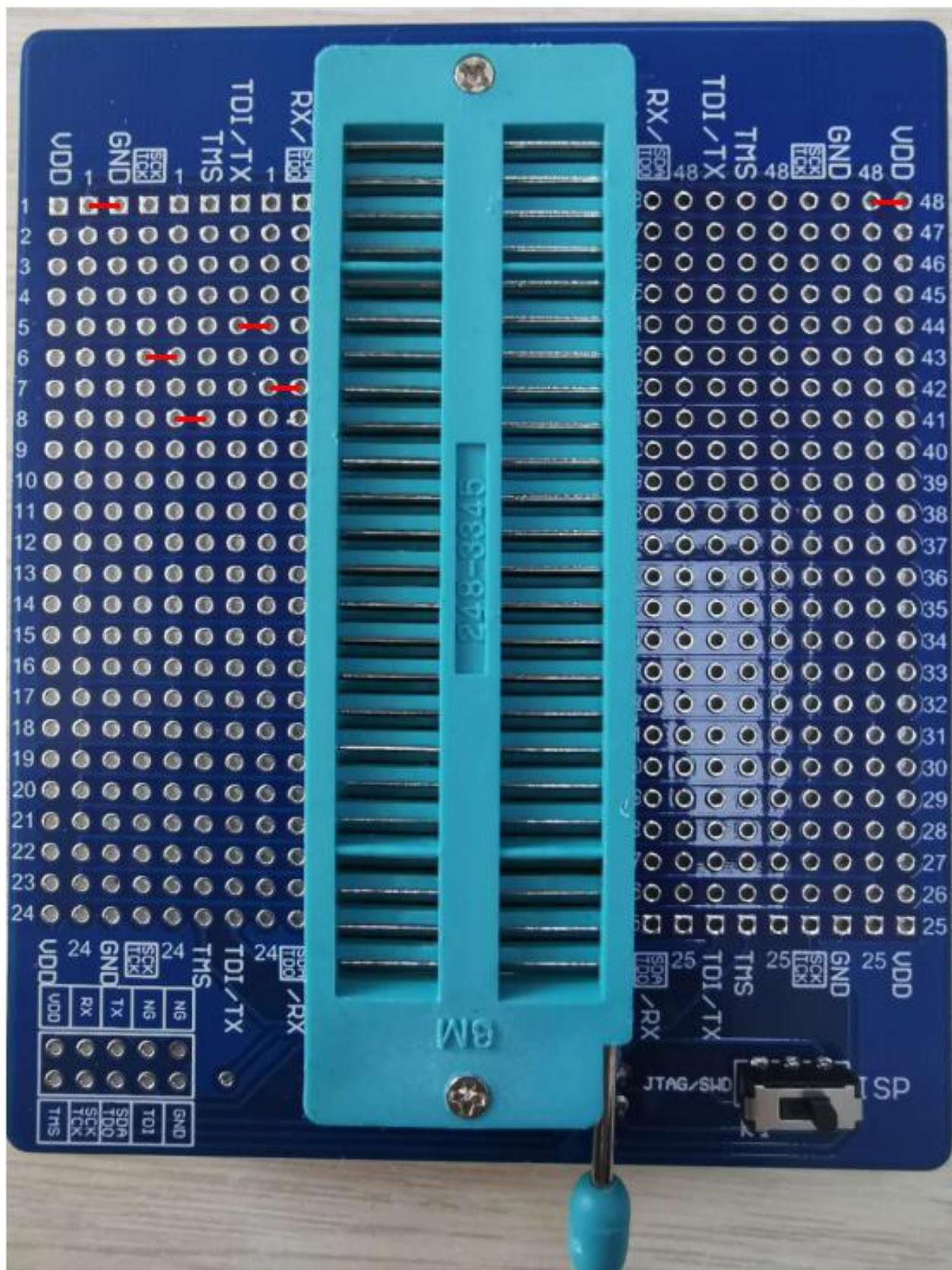
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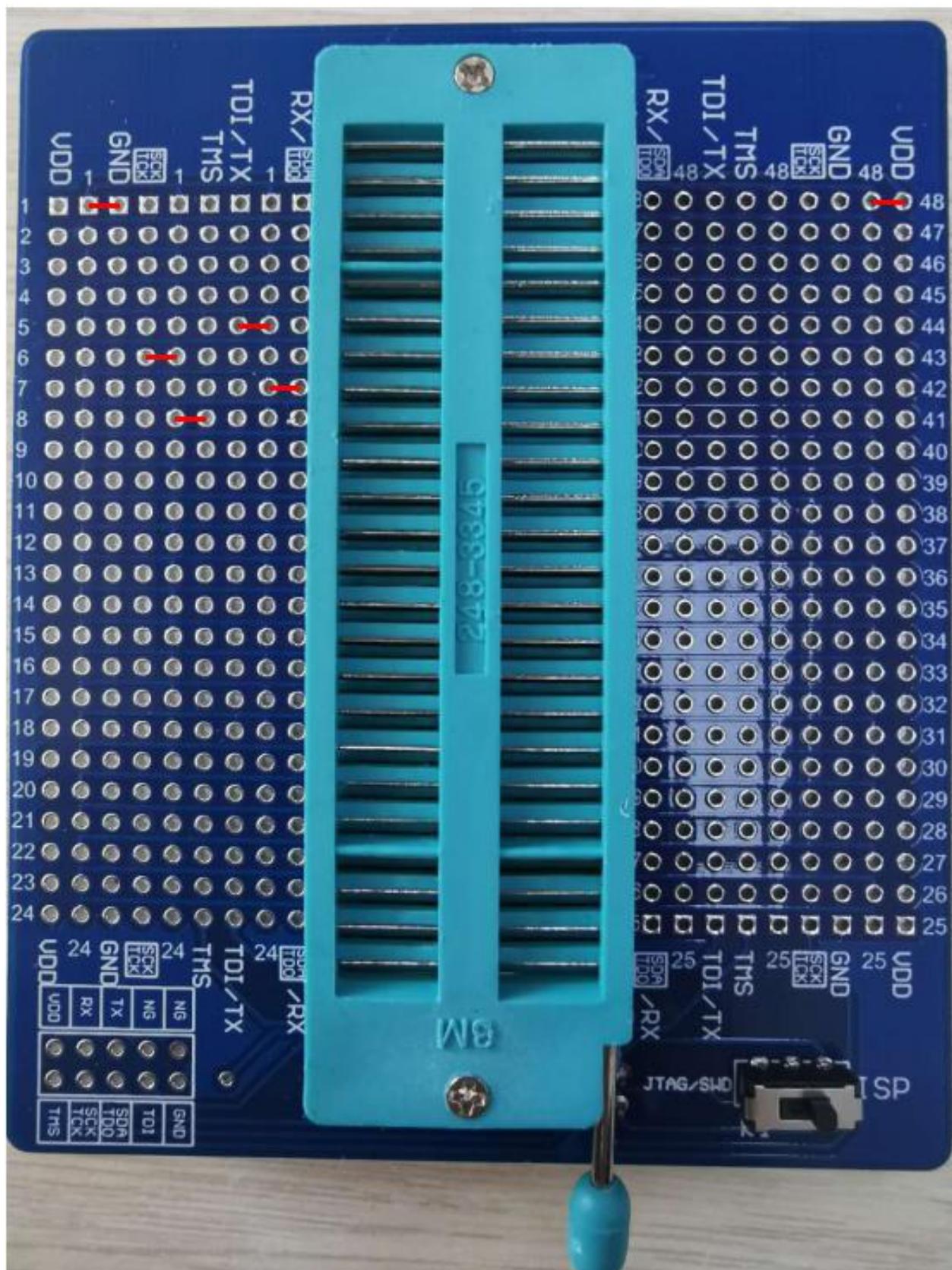
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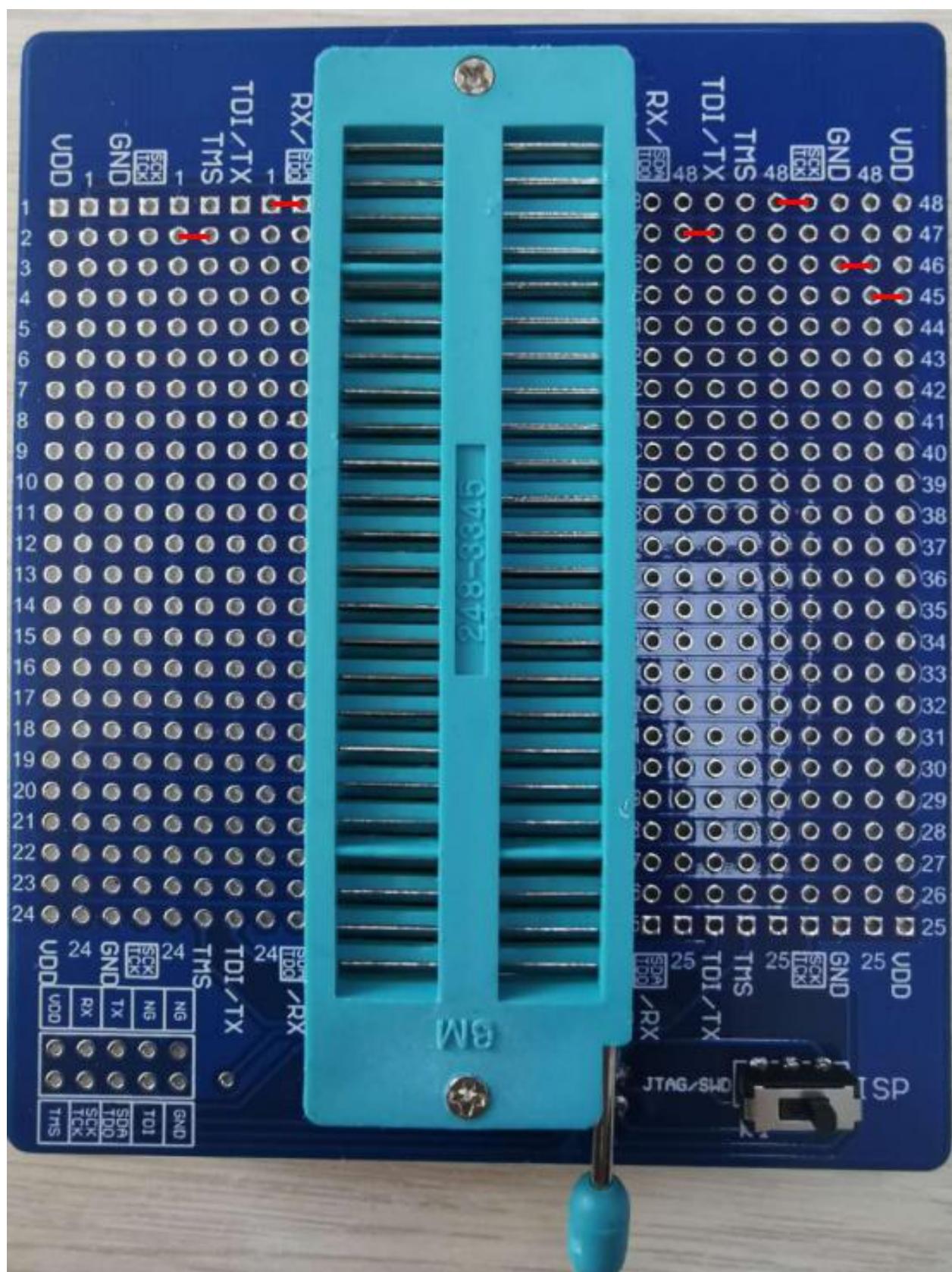
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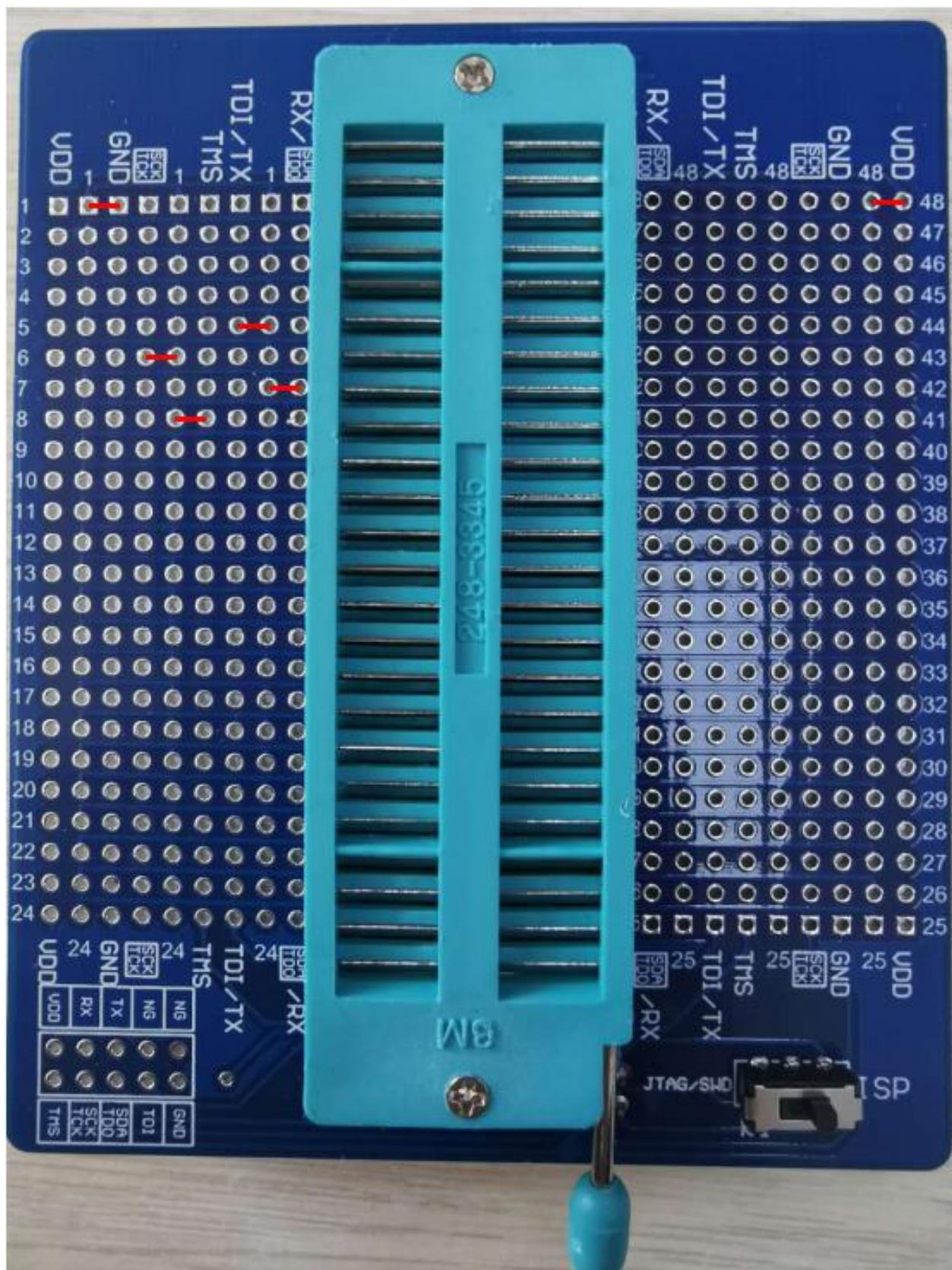
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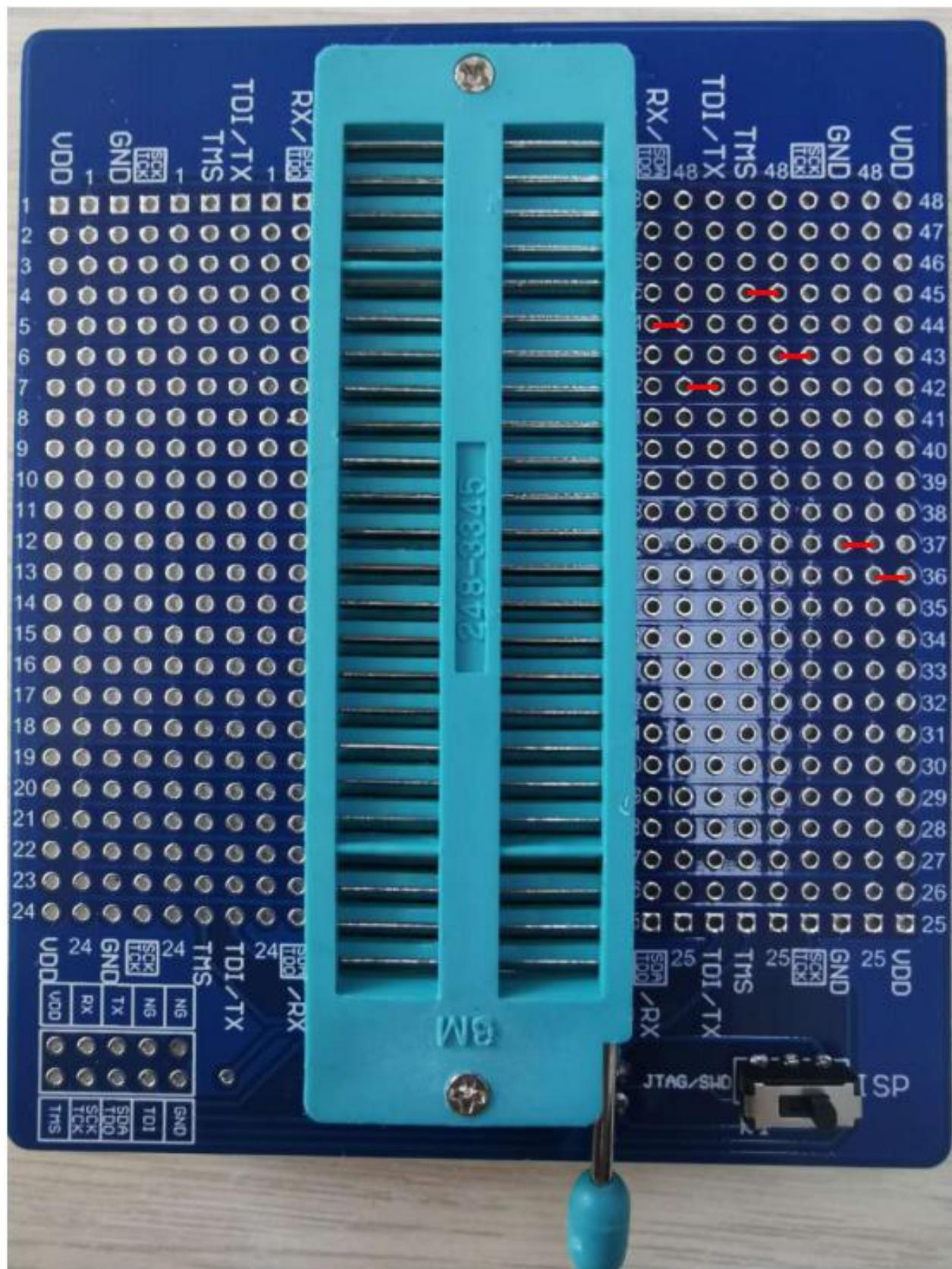
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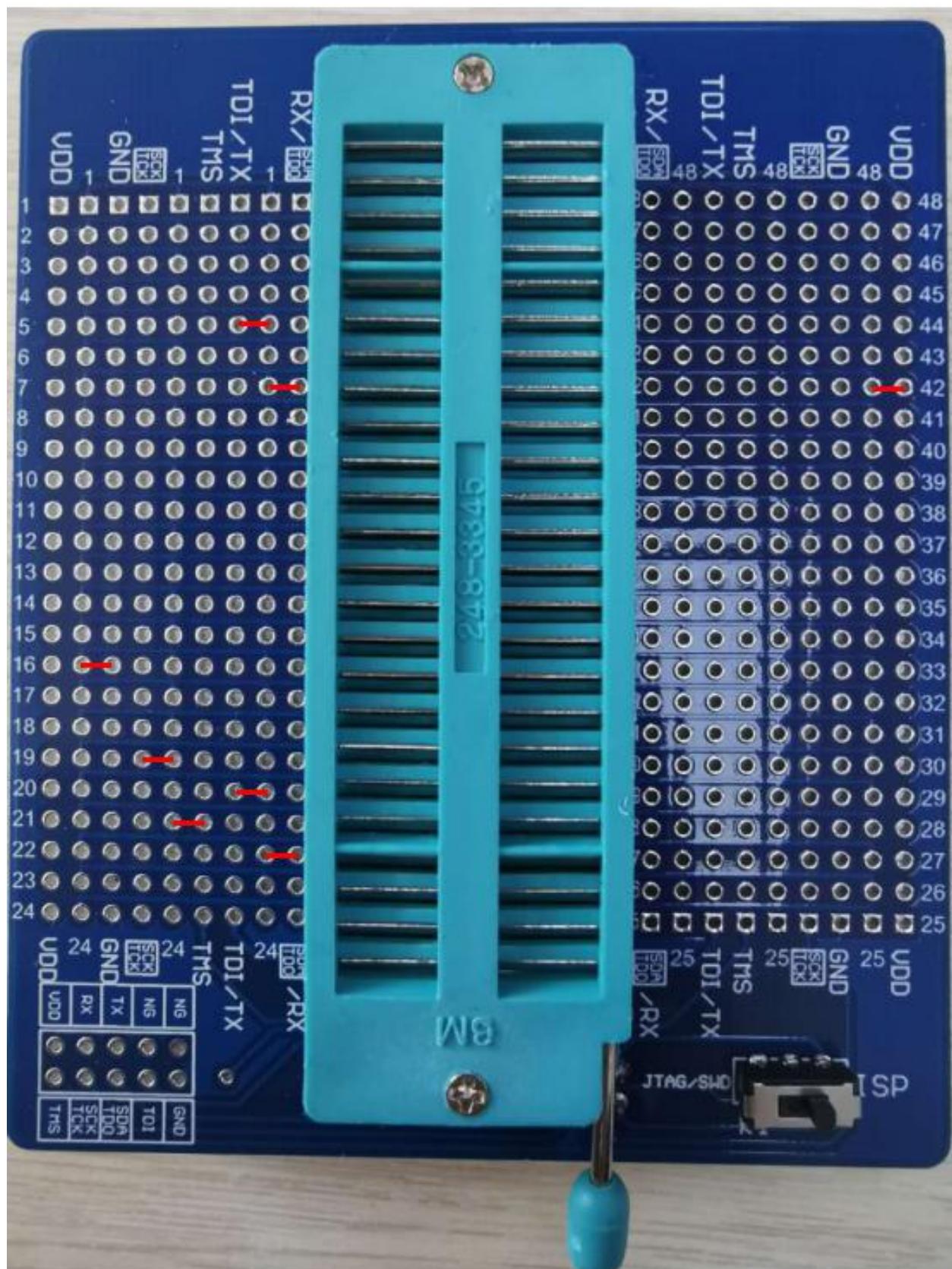
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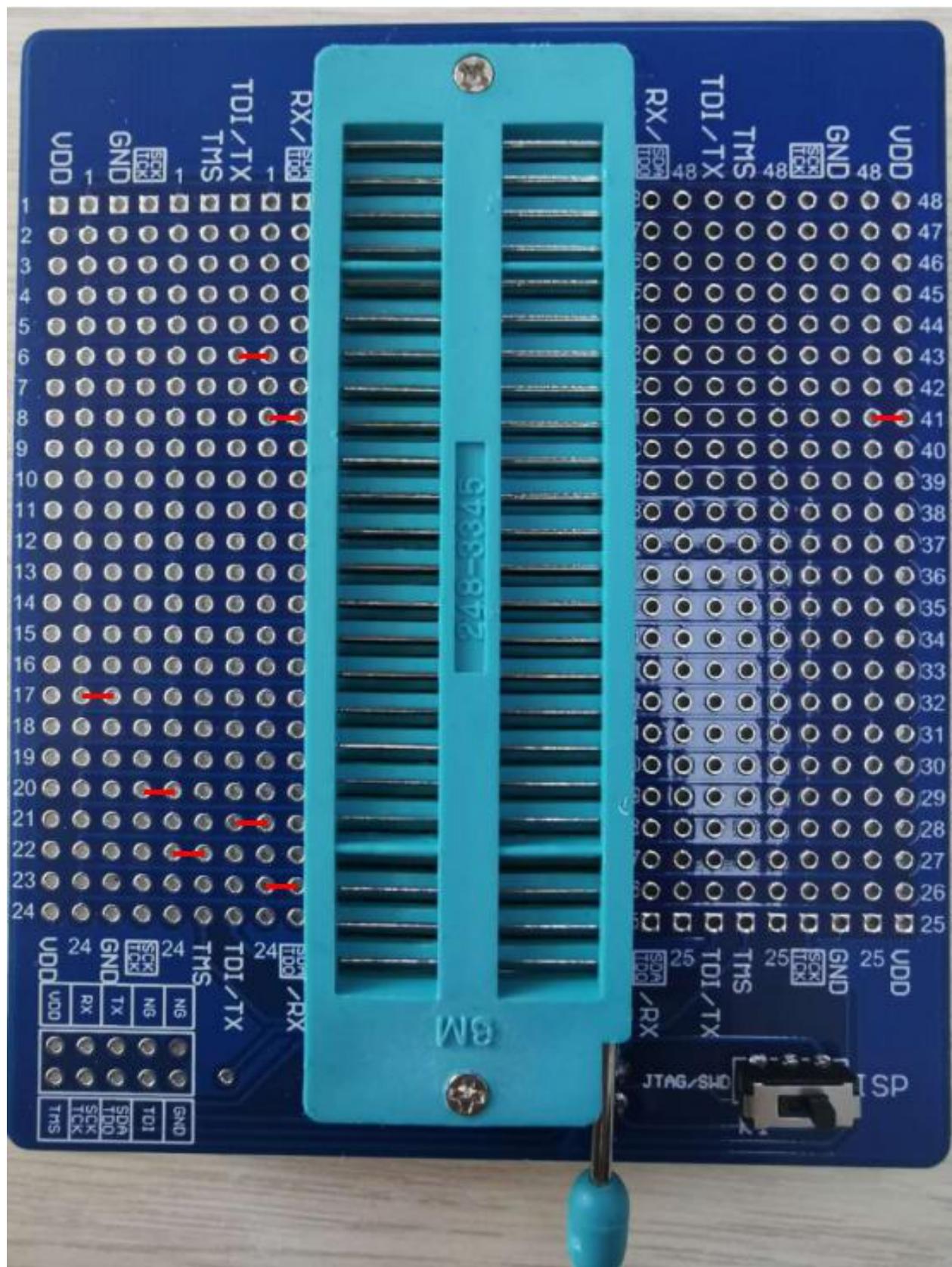
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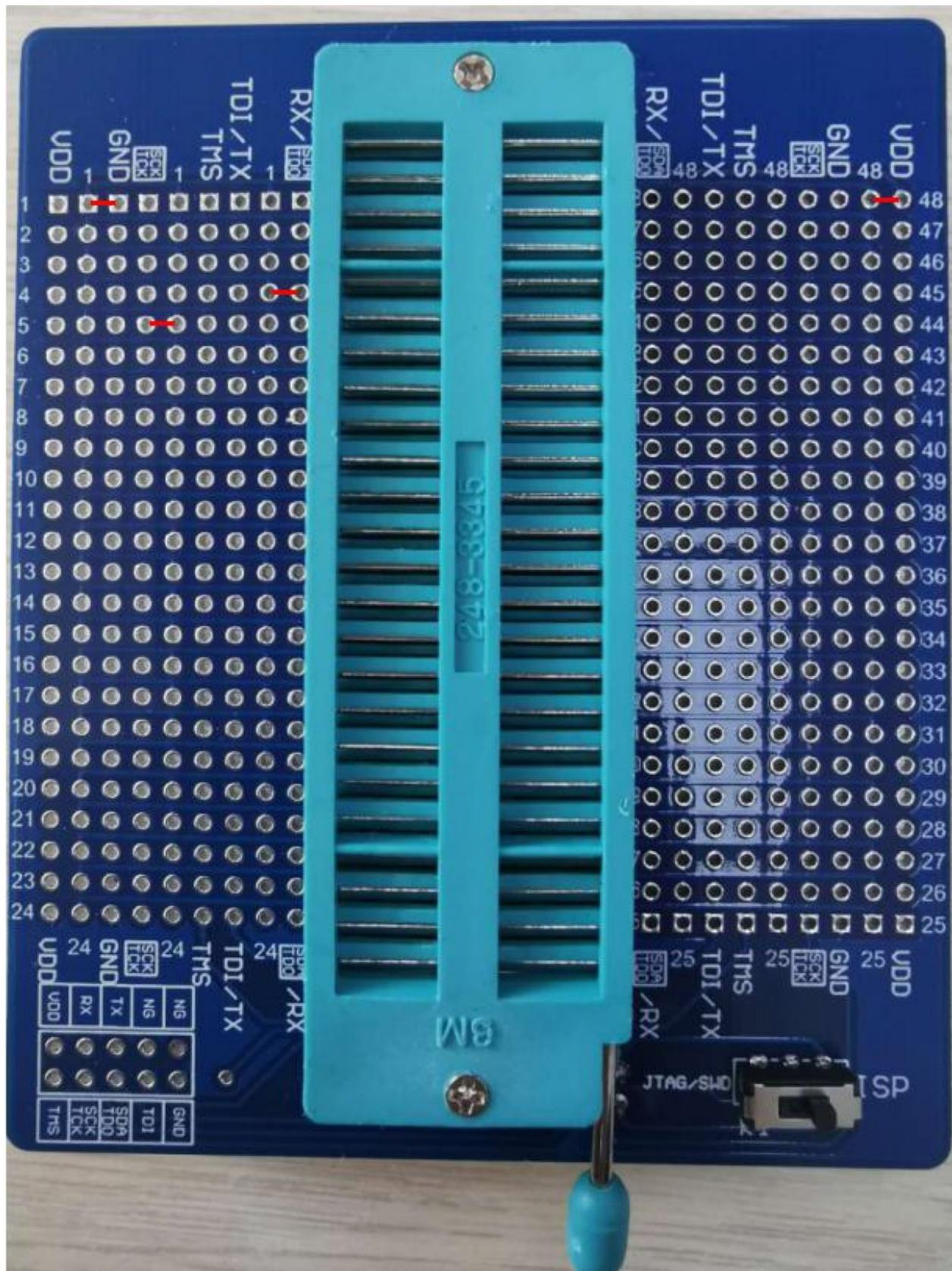
2.17 HC89F0650-LQFP44-ISP&JTAG



2.18 HC89F0650-LQFP48-ISP&JTAG



2. 19 HC8M602-SOP16-SWD



3、 TOUCH

JTAG 烧录引脚: VDD、TMS、TCK、TDO、TDI、GND

SWD 烧录引脚: VDD、SCK、SDA、GND

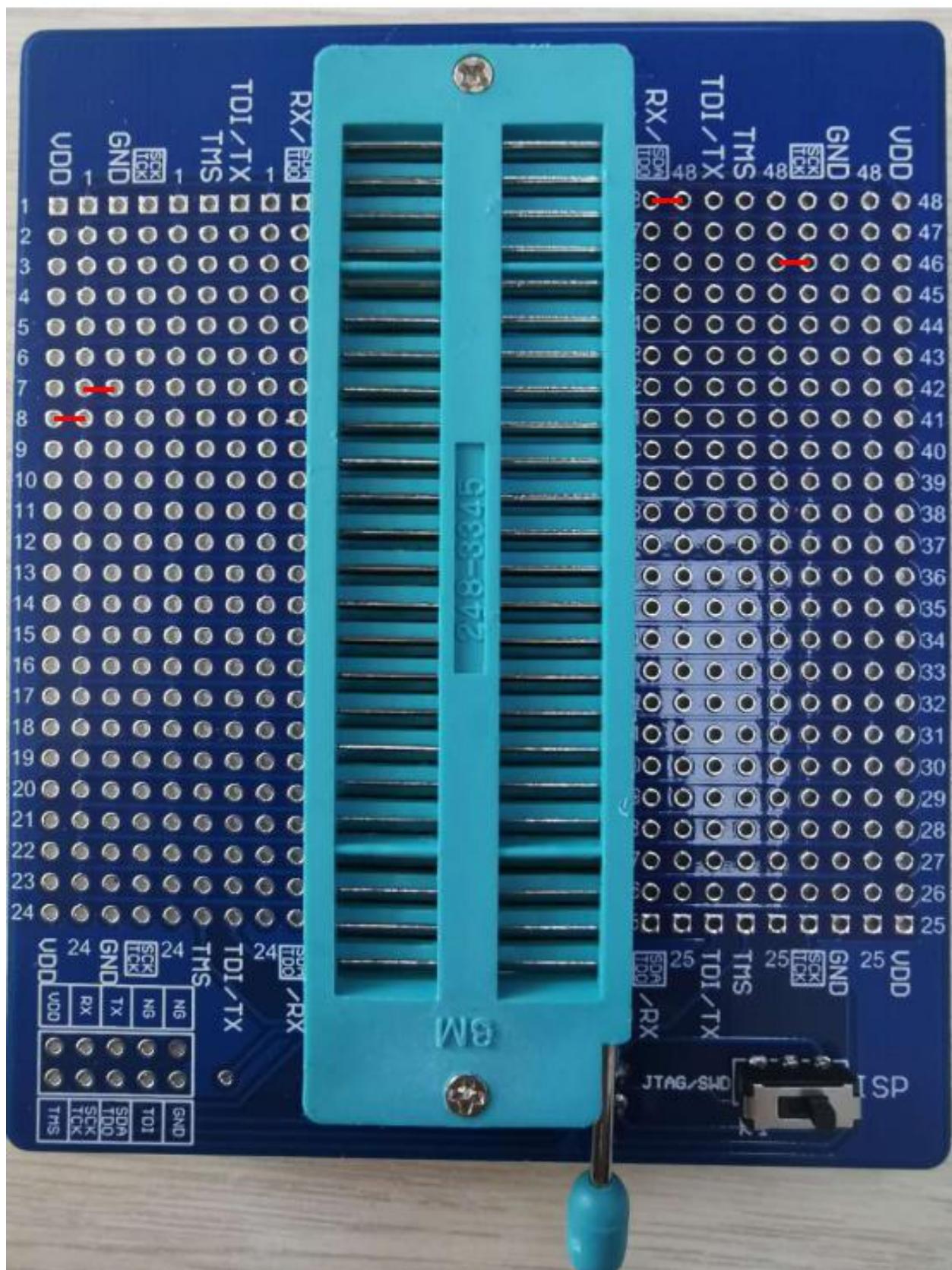
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NC VDD NC NC NC GND NC NC NC NC NC



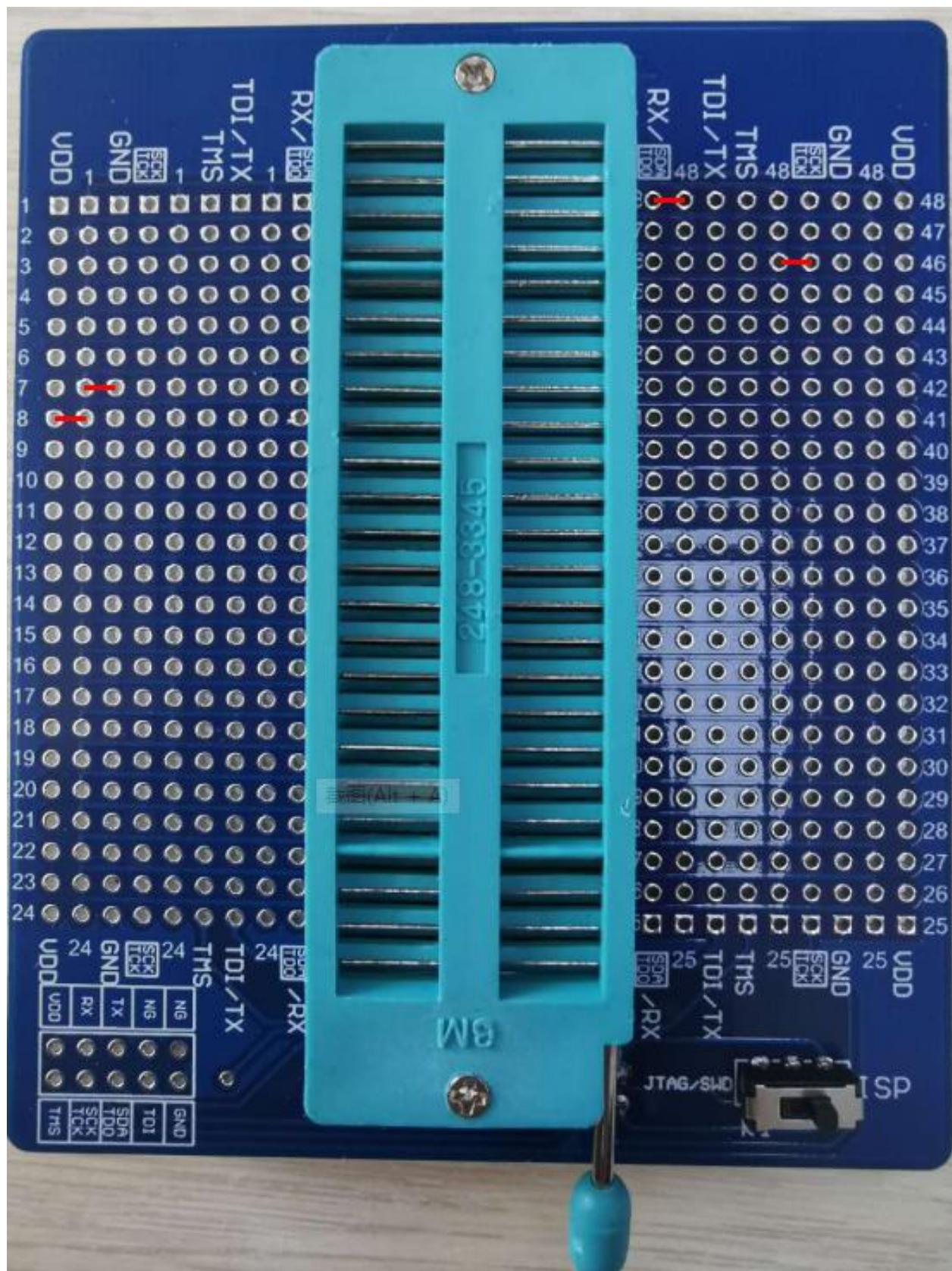
NC NC NC NC NC NC NC TDI TMS ISP_RX
NC NC NC NC NC NC NC TDO TCK ISP_TX
SDA SCK

HC-PM51 V5.0 引脚图

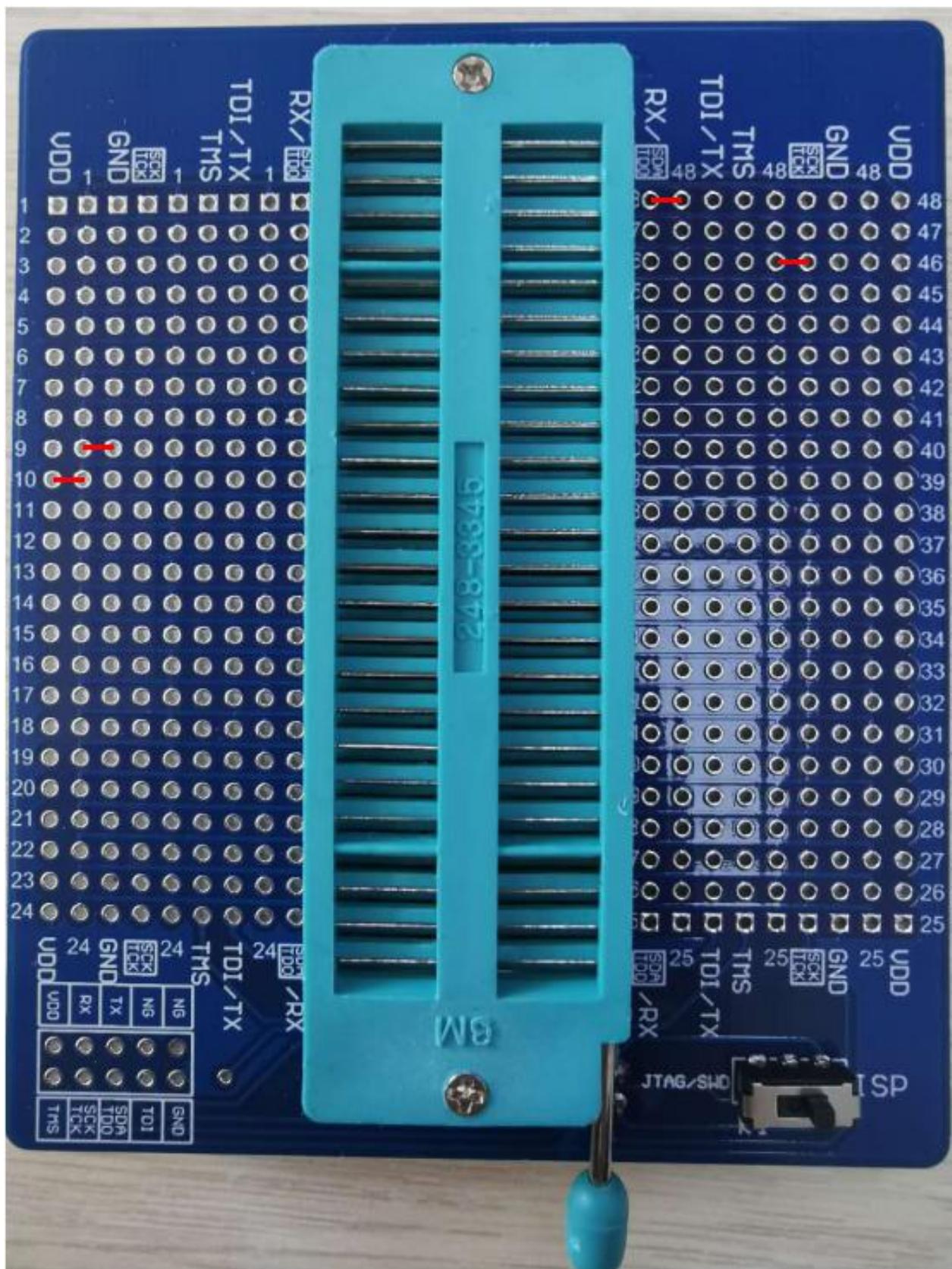
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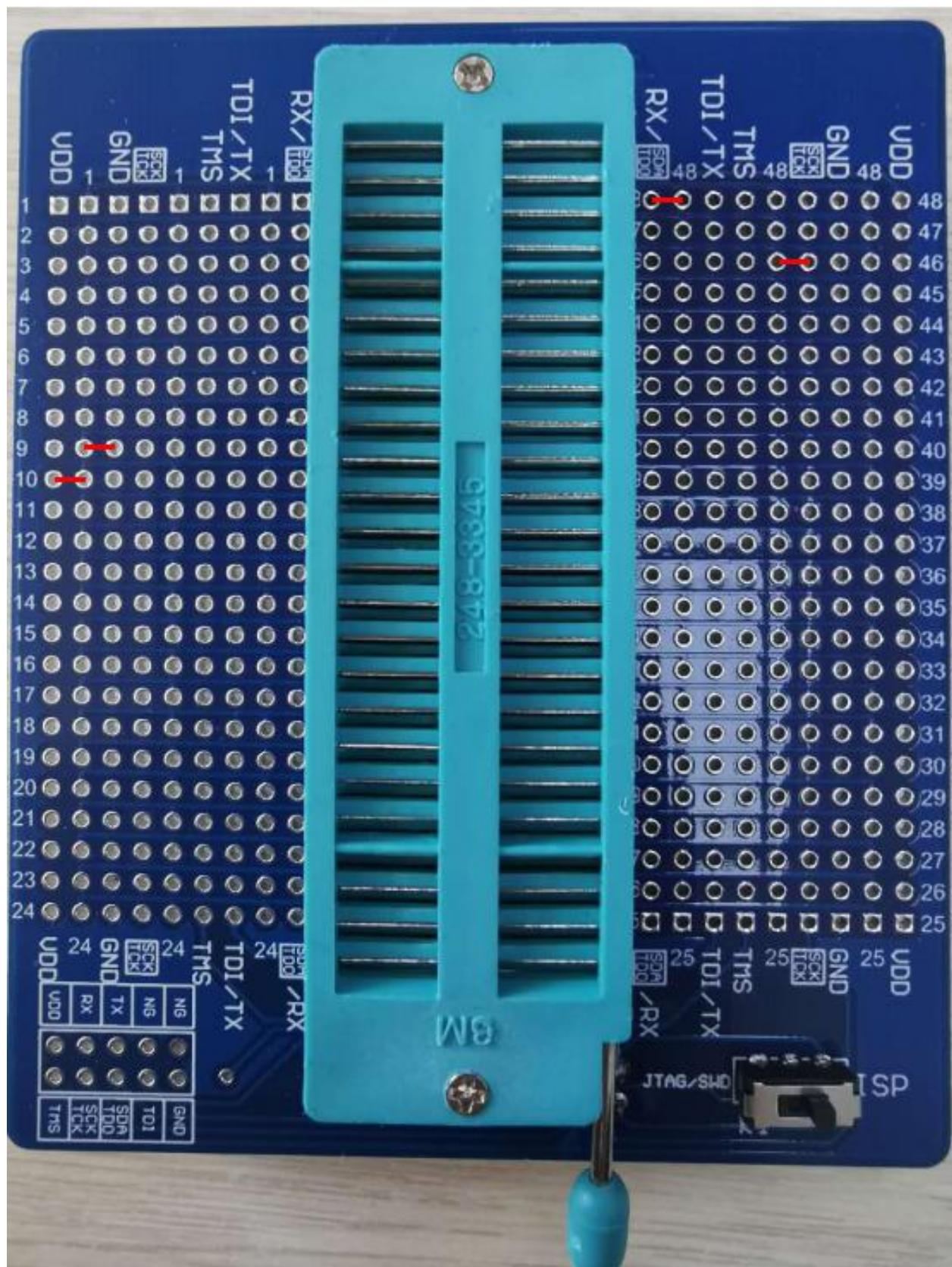
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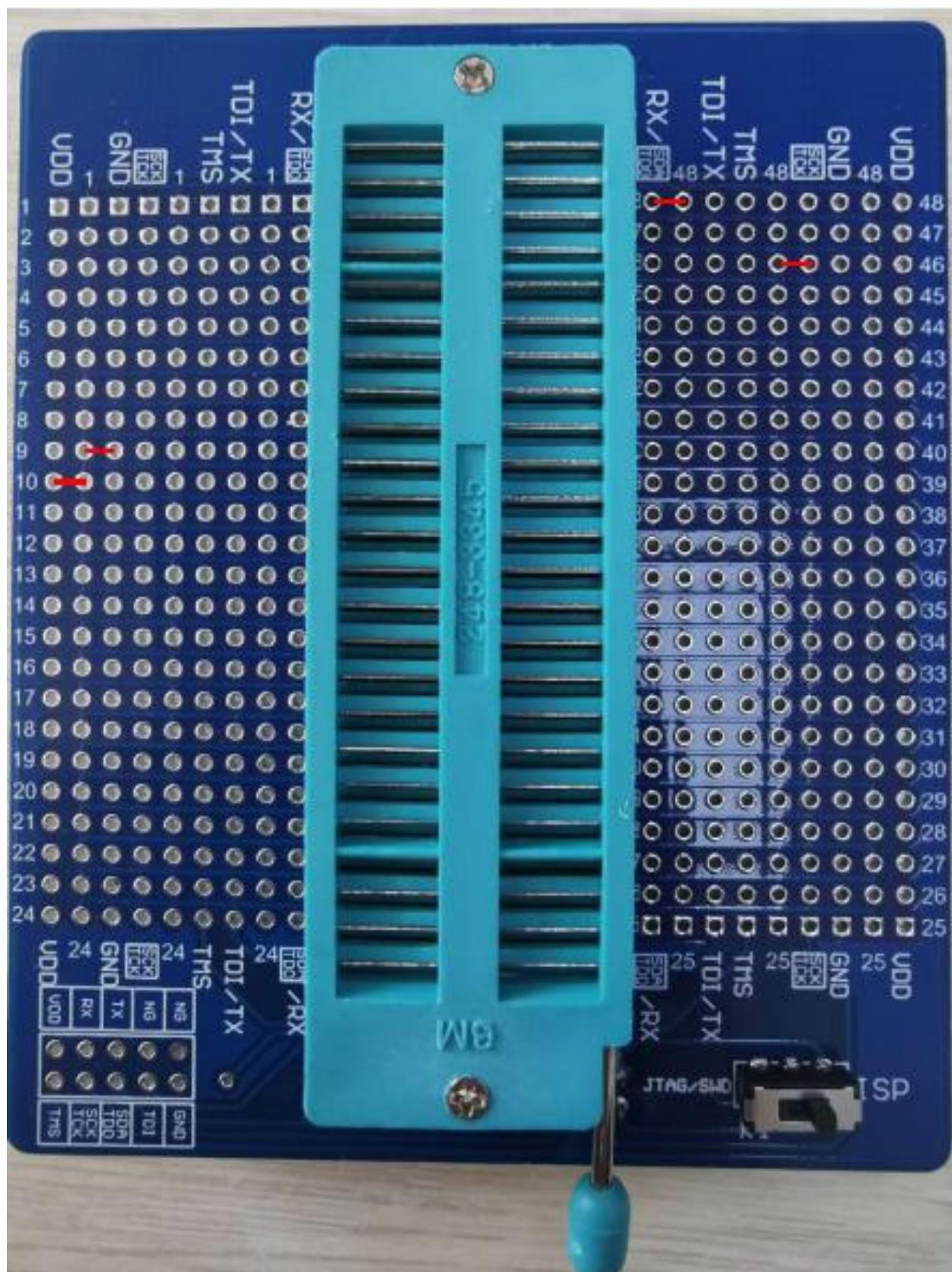
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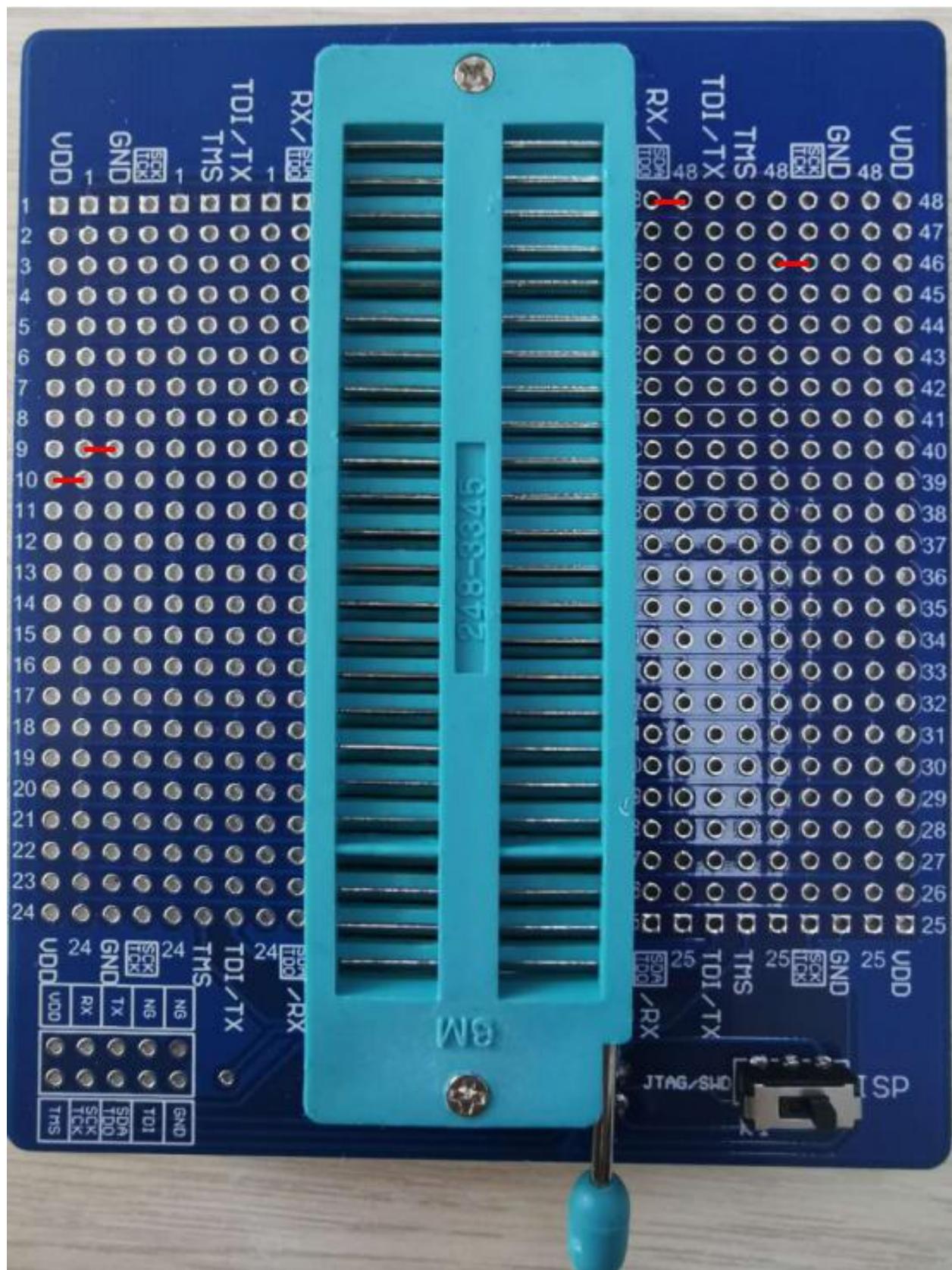
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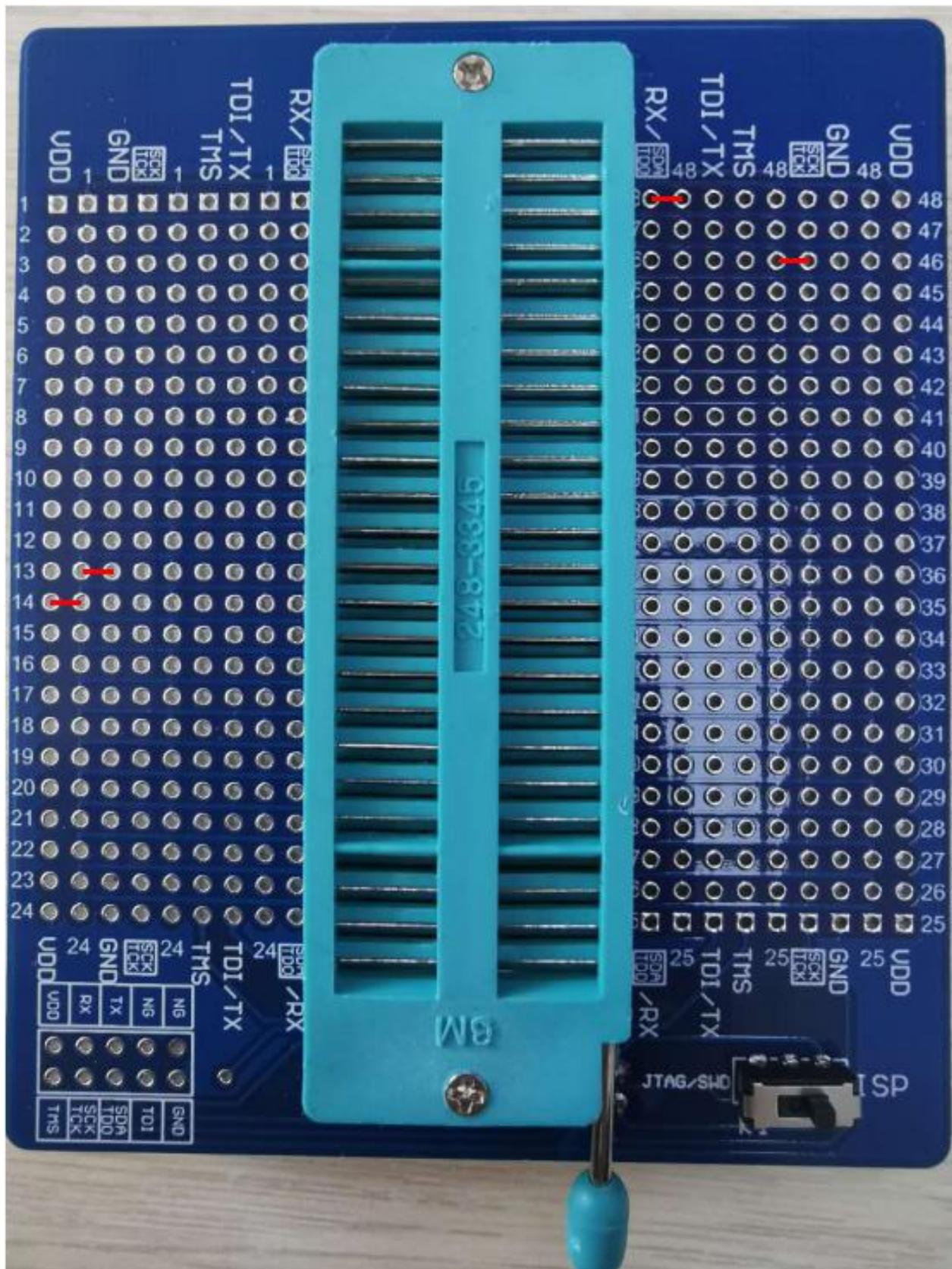
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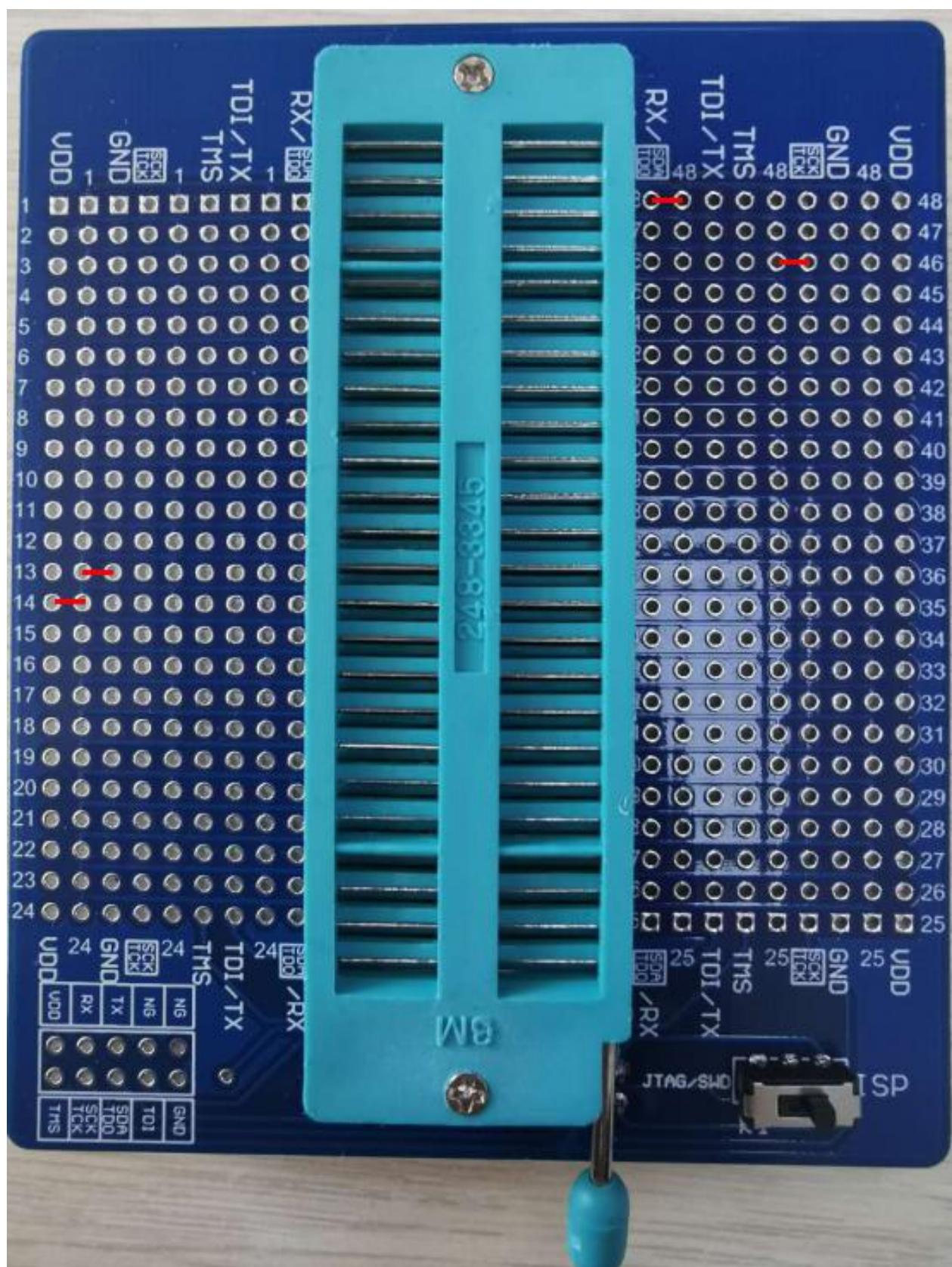
3. 6 HC89F302B-SOP24-SWD



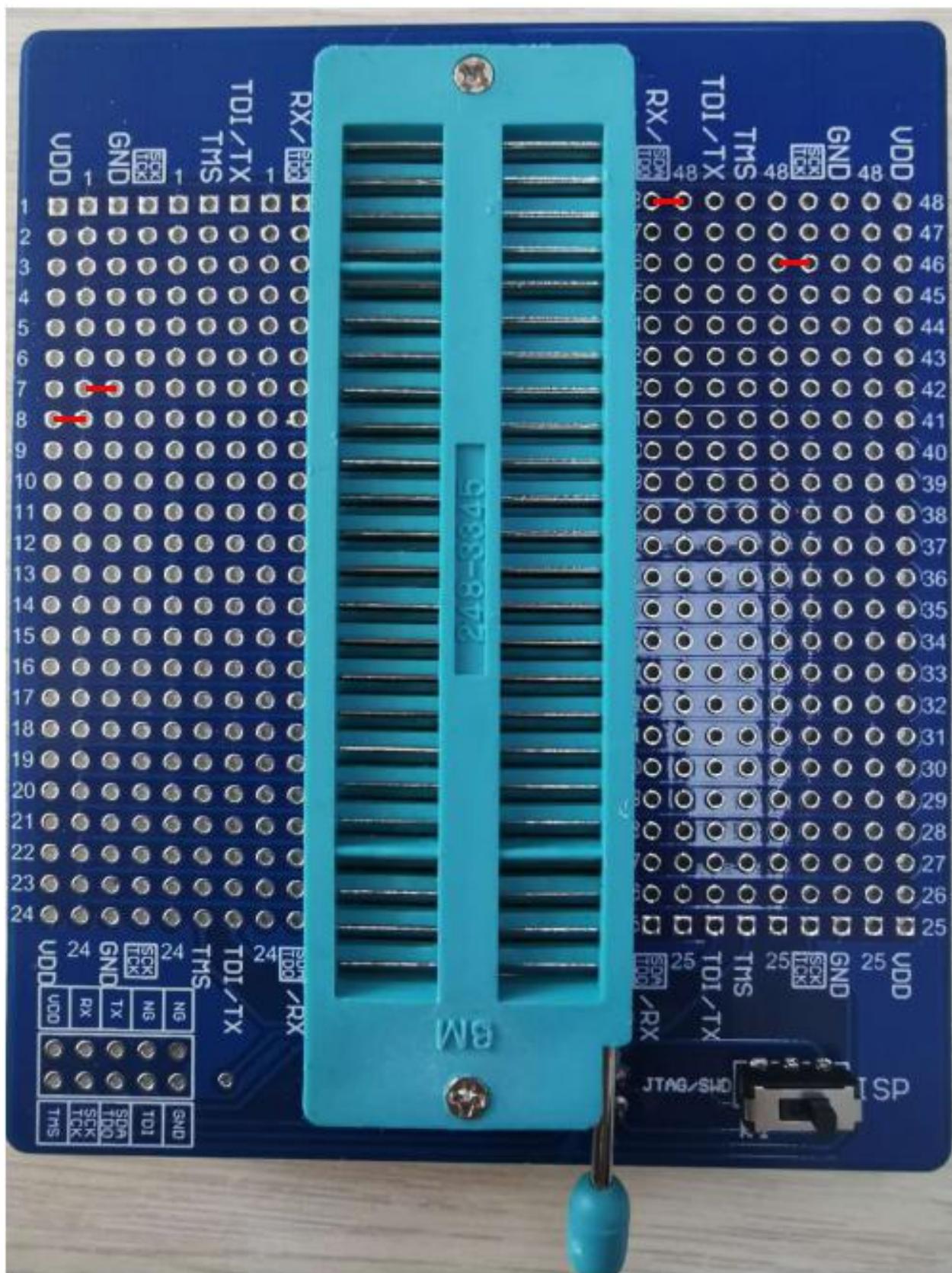
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3. 8 HC89F303B-SOP28-SWD



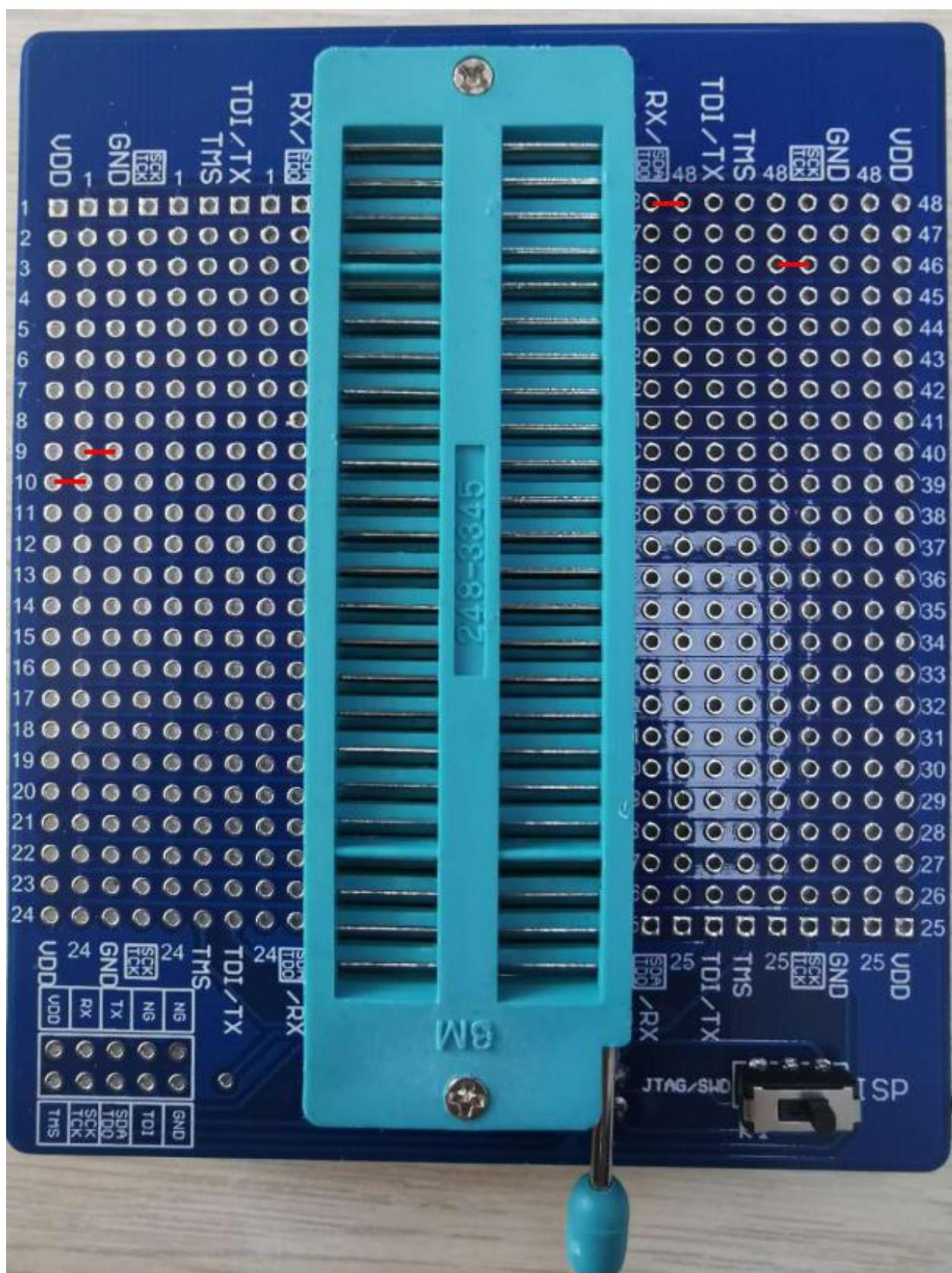
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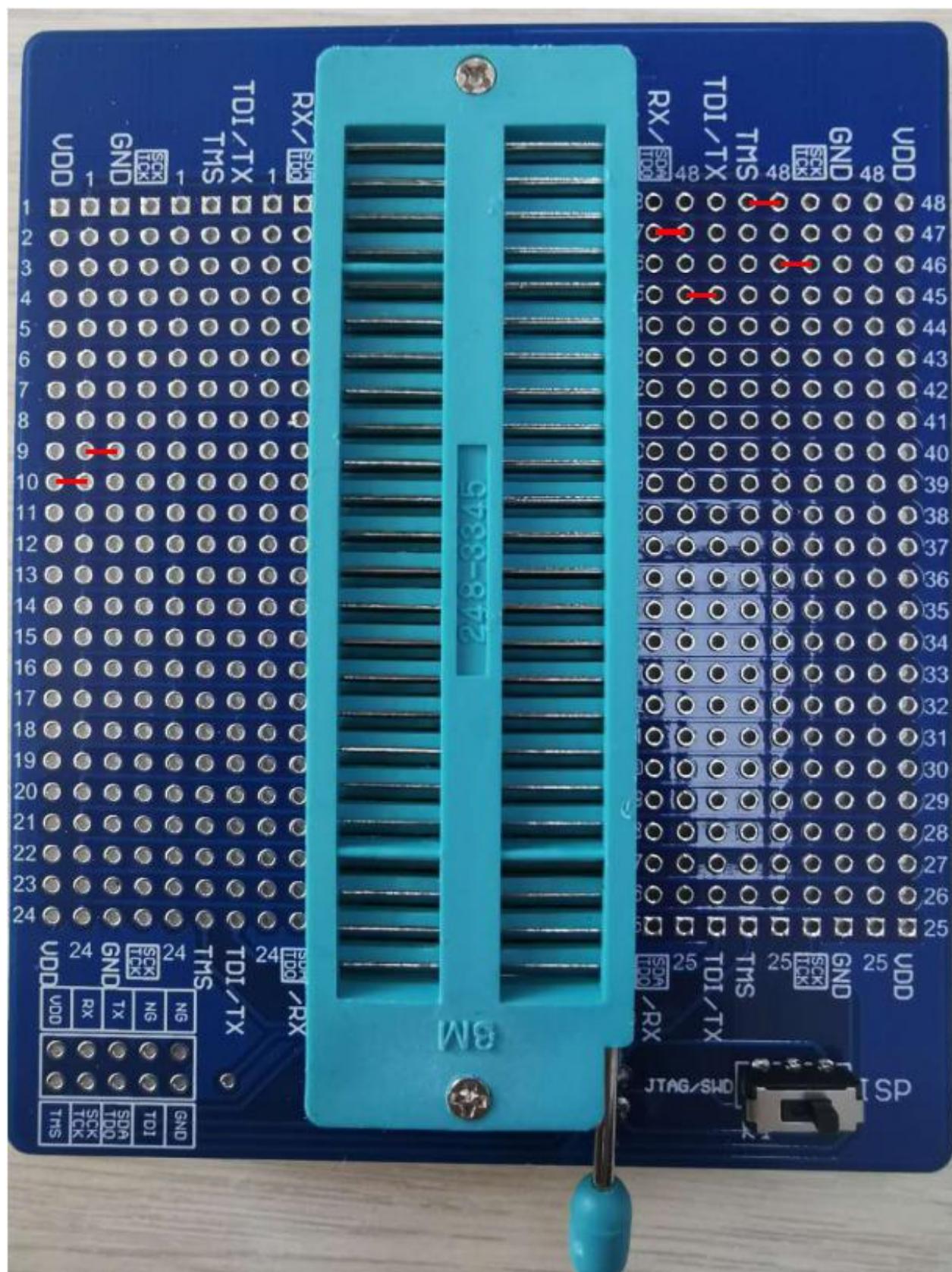
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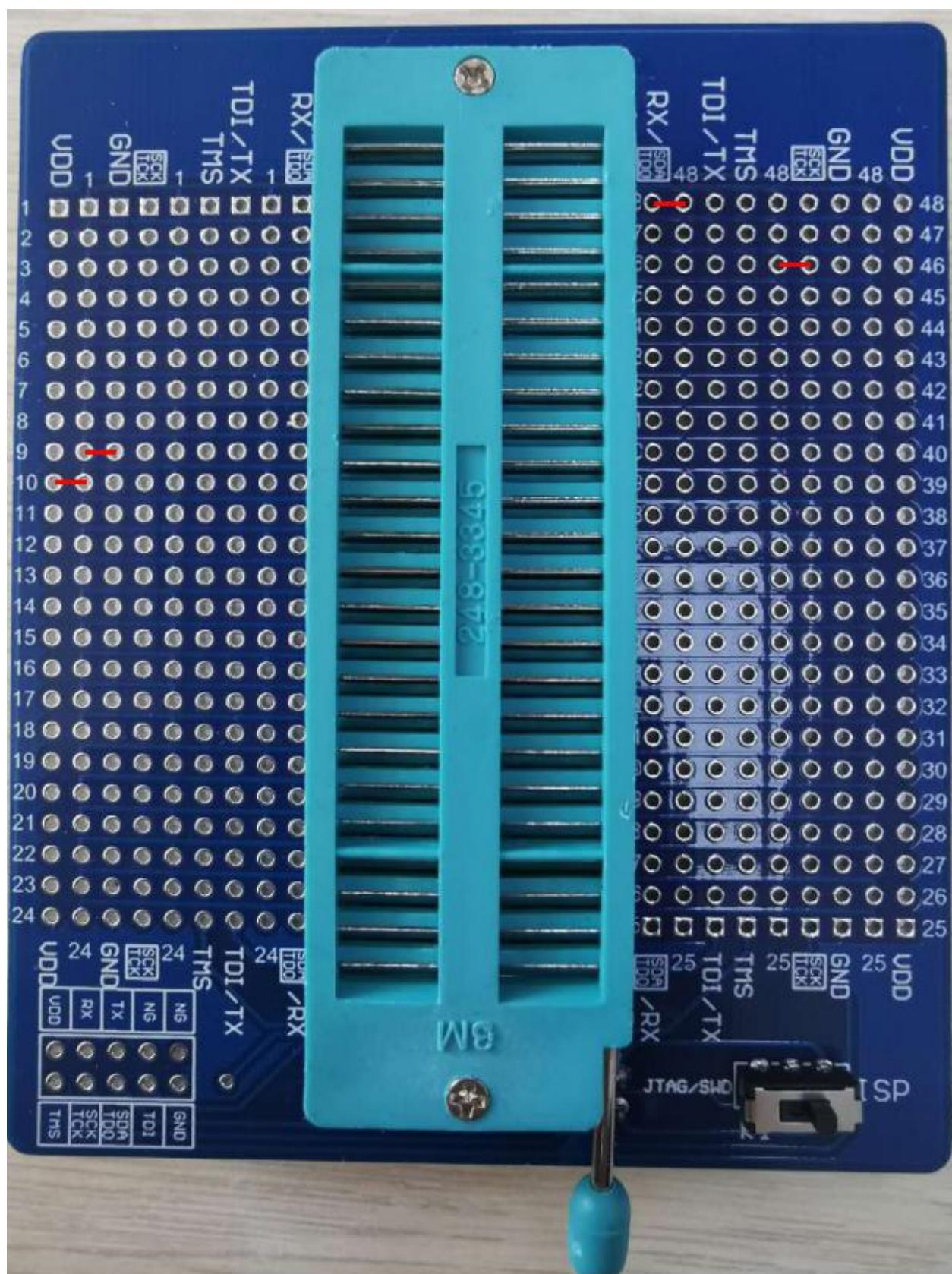
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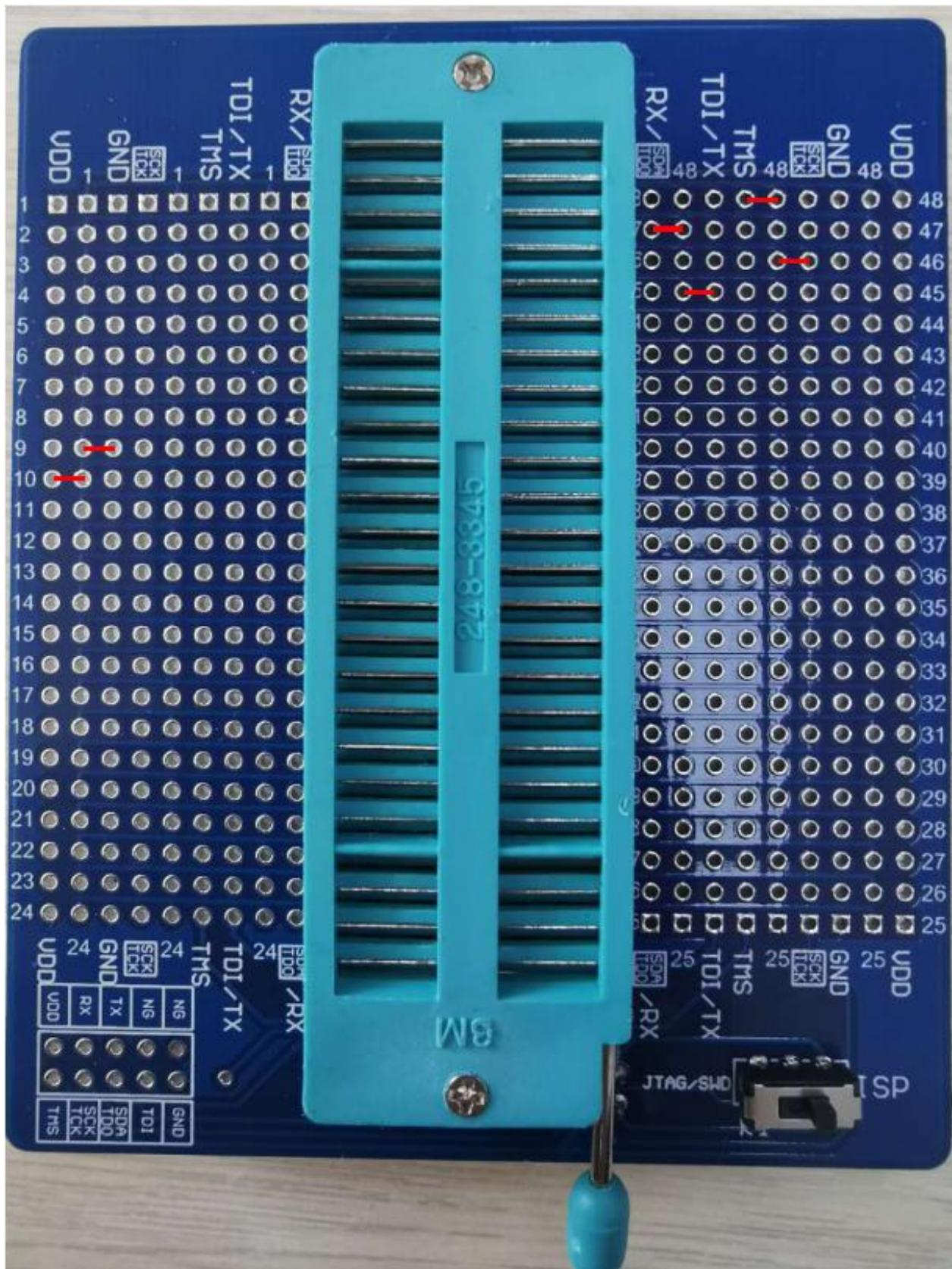
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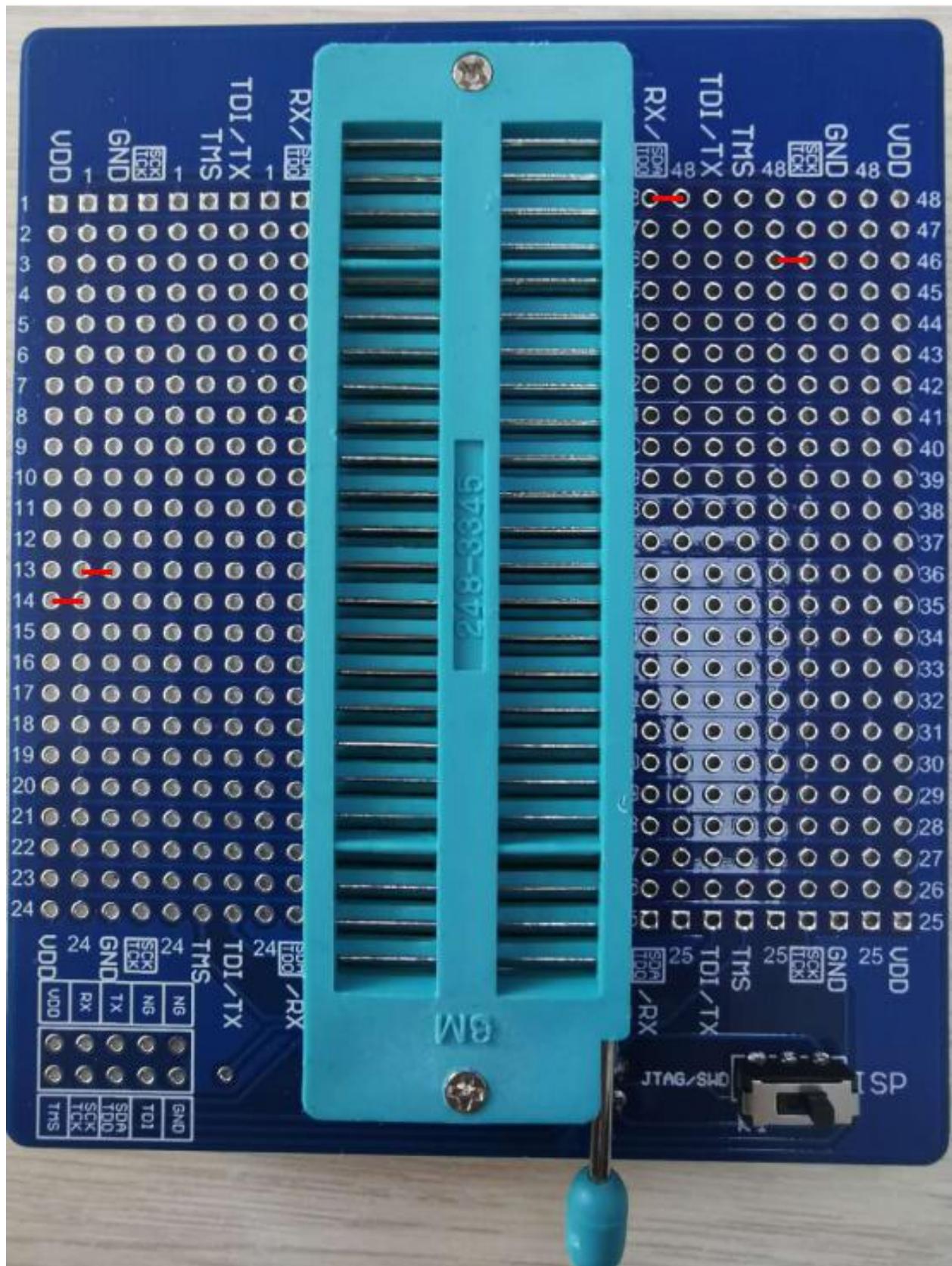
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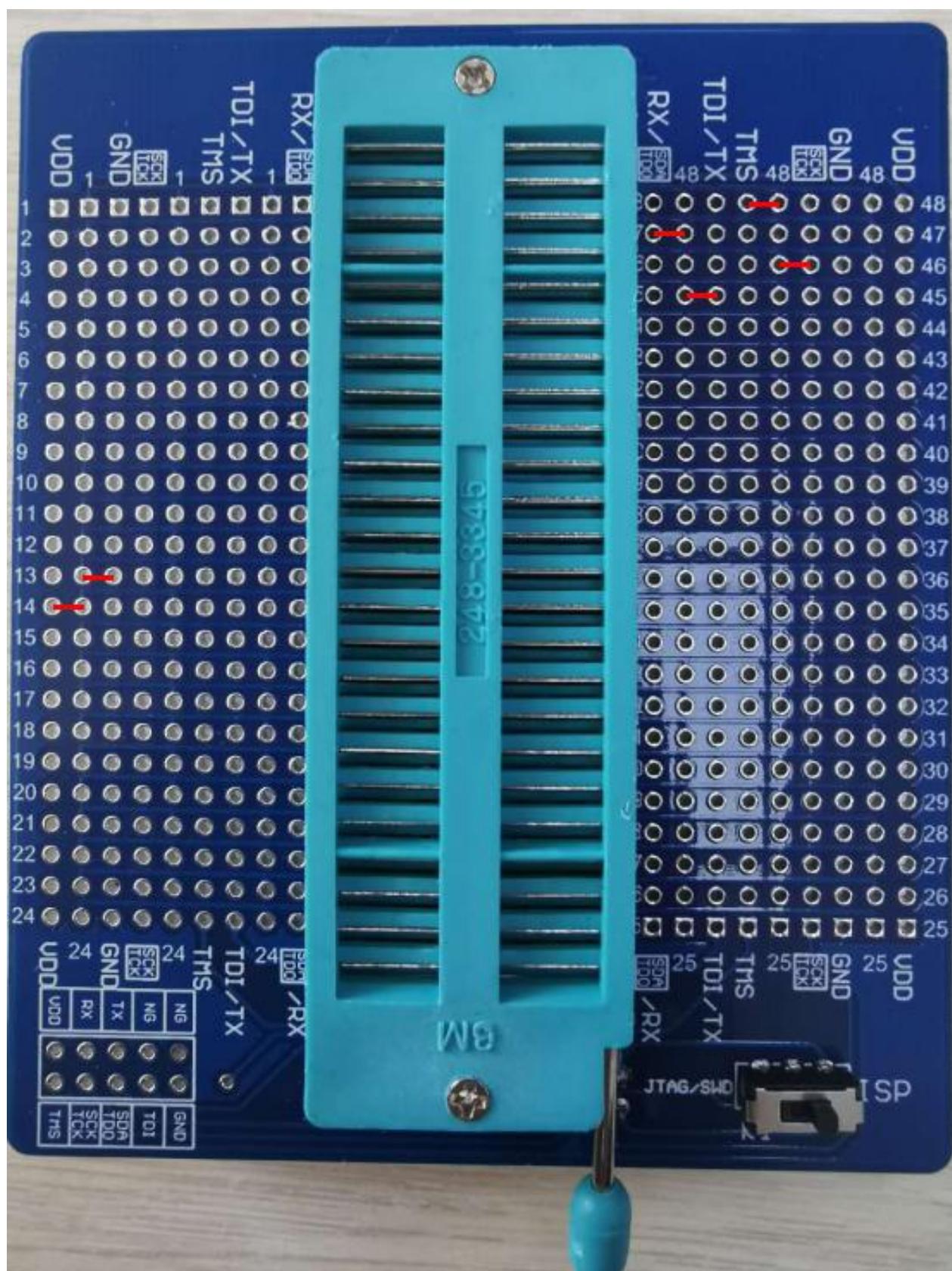
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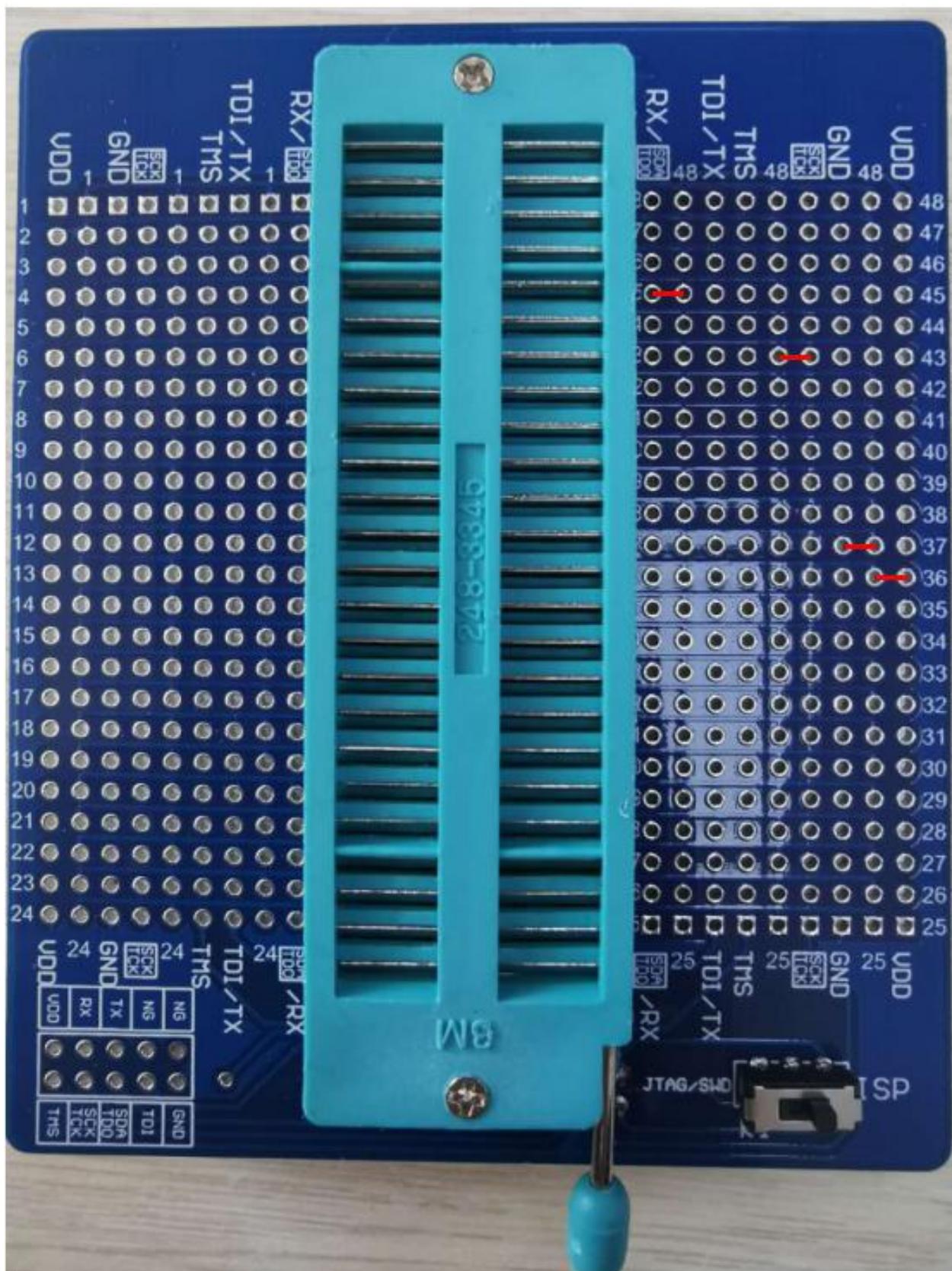
3.15 HC89F3541-SOP28-SWD



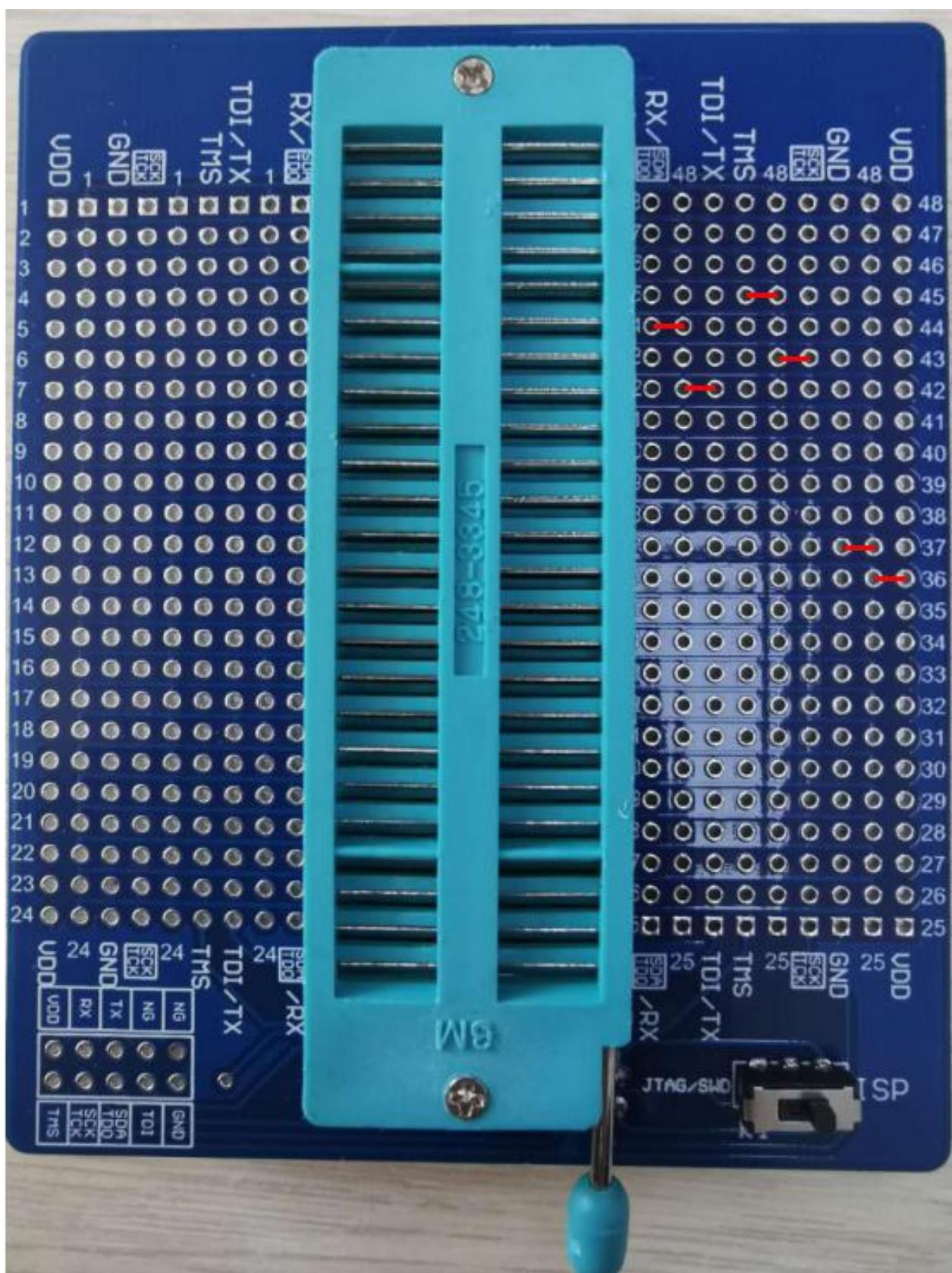
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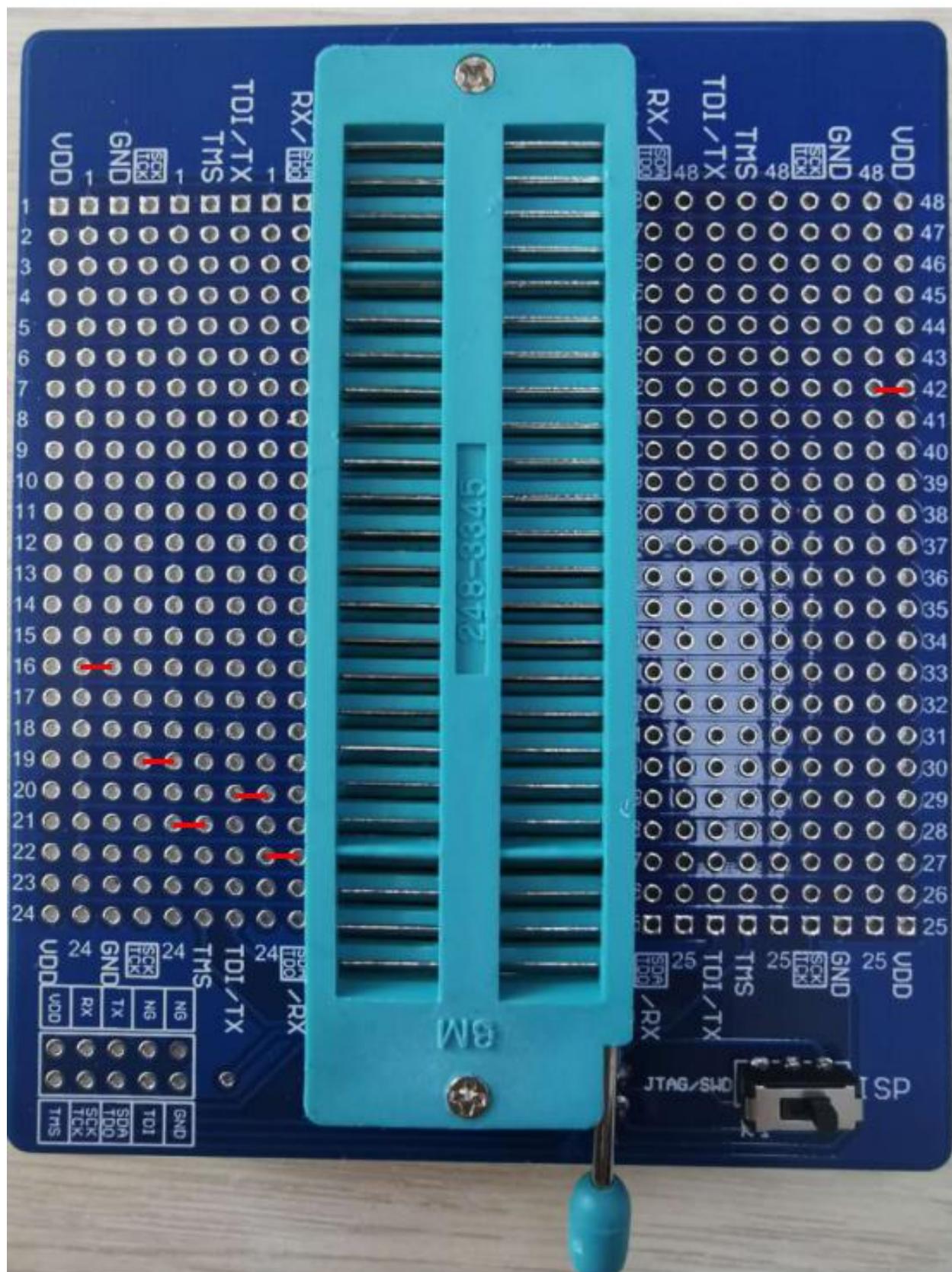
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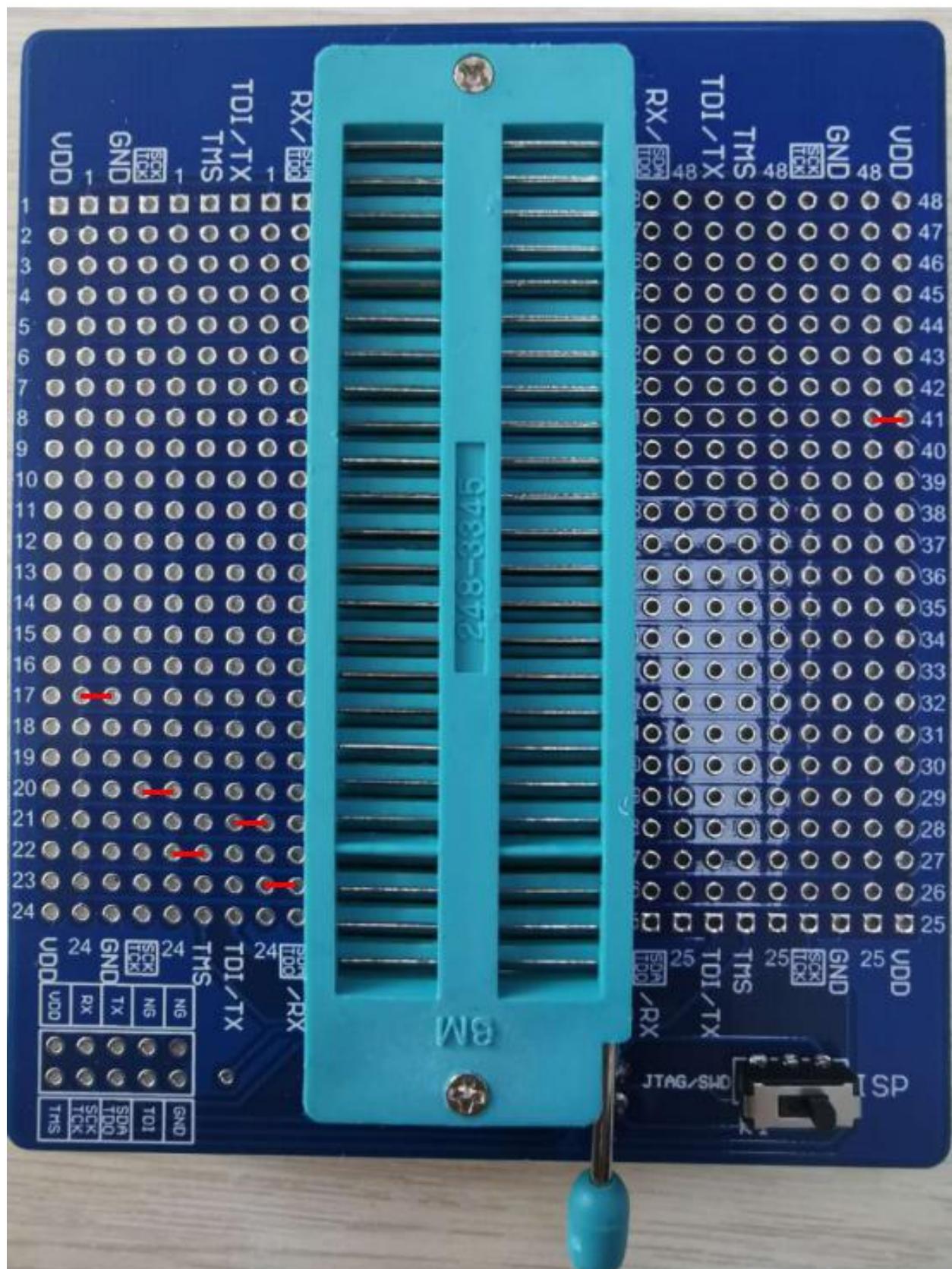
3.18 HC89F3541-LQFP32-JTAG



3.19 HC89F3650-LQFP44-JTAG



3. 20 HC89F3650-LQFP48-JTAG



版本说明

版本	日期	描述
V1.00	2020/03/30	初版

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芯圣电子

2020年3月